

Customer Code: _____

DATASHEET

DAPU P/N: **DP7X4800006**

DAPU			Customer Approval
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2020.06.24			

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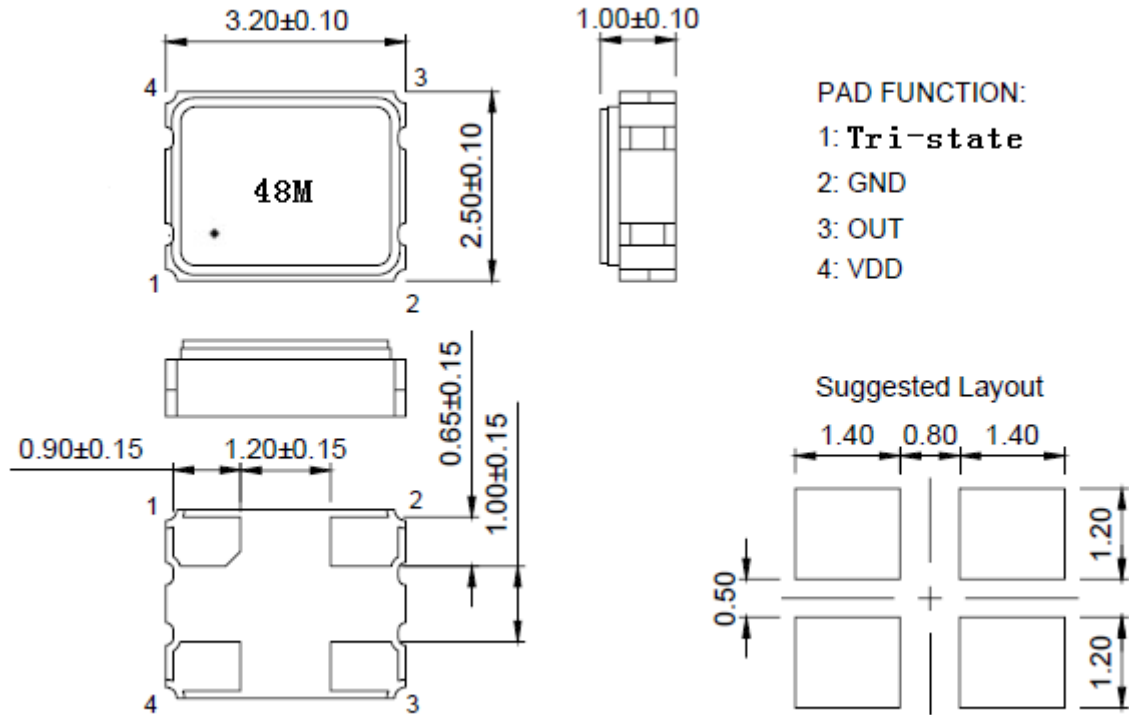


1、Electrical Parameters

MODEL: DP7X4800006							
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL	48.00			MHz	
2	Output Waveform		CMOS				
3	Symmetry	-	45	~	55	%	
4	Frequency stability	-	±20			× 10 ⁻⁶	Inclusive of all conditions: Calibration Tolerance at 25 °C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25 °C, Shock, and Vibration
5	Operating Temperature	Topr	-40	25	+85	°C	
6	Storage Temperature	Tstg	-55	~	+125	°C	
7	Supply Voltage	VDD	2.97	3.3	3.33	V	
8	Input Current				36	mA	
9	Output Load:	CL	15			pF	
10	Output Voltage High	VoH	90% Vcc	-	-	V	
11	Output Voltage Low	Vol	-	-	10% Vcc	V	
12	Aging	-	±3			ppm/yr.	1st. Year at 25°C
13	Start up time				6	ms	
14	Rise/ Fall Time	Tr/Tf	-	-	5	ns	
15	Enable Control		Yes				Pad1,Output Enable/Disable.
16	Disable Current	Ist			400	µA	
17	RMS Phase Jitter				1	ps	Integrated from 12KHz to 20MHz
18	ESD Level	Human Body Model,class2: 2000V to 4000V; ANSI/ESDA/JEDEC JS-001-2010.					
		Machine Model, class B: 200V to 400V; JEDEC JESD22-A115C.					
19	Moisture Sensitivity Level	Level 3.					
20	Vibration	Test Condition: 0.75mm ;acceleration:10g;10Hz~2000Hz, one cycle per 30 min, test 2 hour. (3 times for each 3 directions X ,Y , Z) .IEC 68-2-06 Test Fc.					
21	Shock	100g; 6ms; half sine wave (3 times for each 3 directions X ,Y , Z),IEC 68-2-27 Test Ea/Severity 50A.					
22	Full Package Storage	Relative humidity (%)		20% ~70%			
		Temperature (°C)		-10~35°C			



2、Mechanical Structure(m



Note1: Tolerance ±0.2mm

Note2: When “Enable control” goes low, the frequency output becomes high impedance.

Pin1	Output	Oscillator
High (or open)	Frequency output	Oscillation
Low	High Impedance	Oscillation