

**Customer Code:** \_\_\_\_\_

# DATASHEET

**DAPU P/N:** DPC32200M000AC10NA0

<b>DAPU</b>			<b>Customer Approval</b>
Drew	Audited	Approved	
Jieshu ZHENG	Jianhua LIN	Gangtao FENG	
Date:	2024/5/10		

Stamp, please! Thanks!

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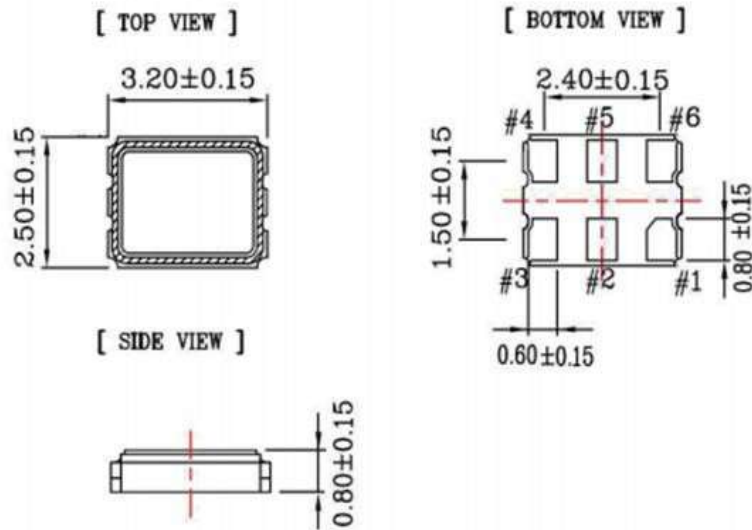


**1、Electrical Parameter**

MODEL :		DPC32200M000AC10NA0					
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL	200.000			MHz	
2	Oscillation Mode	-	3rd				
3	Frequency Stability (Overall)	-	-25		25	ppm	Frequency stability includes frequency tolerance@25°C and frequency stability vs. operating temperature range and voltage variance and first year aging.
4	Operating Temperature	Topr	-40		105	°C	
5	Storage Temperature	Tstg	-55		125	°C	
6	Supply Voltage	VDD	3.135	3.3	3.465	V	VDD±5%
7	Current	Icc			100	mA	At maximum supply voltage.
8	Output waveform	-	LVDS				
9	Output Load	CL	100			Ω	
10	Output Voltage High	VOH			1.6	V	
11	Output Voltage Low	VOL	0.9			V	
12	Rise Time	Tr			1	ns	
13	Fall Time	Tf			1	ns	
14	Tri-State Output Active	-	1.75 or Floating			V	Pin 1 Tri-state Enable High
15	Tri-State Output in High-Impedance state	-			0.75	V	
16	Duty Cycle	-	45~55			%	
17	Start-Up Time	Tstart			3	ms	Measured from the time VDD reaches its rated minimum value.
18	Phase Jitter(RMS)				1000	fs	12kHz to 20MHz
19	Phase Noise	-			-90	dBc/Hz	100Hz offset
					-120		10KHz offset
					-135		1MHz offset
					-140		10MHz offset

## 2、 Mechanical Structure

### 2.1 Dimensions

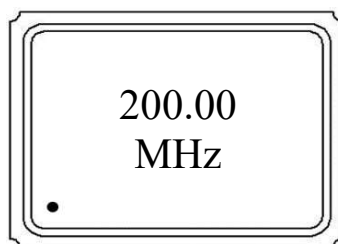


To ensure optimal oscillator performance, place a by-pass capacitor of 0.1uF as close to the part as possible between +VDD and GND pads.

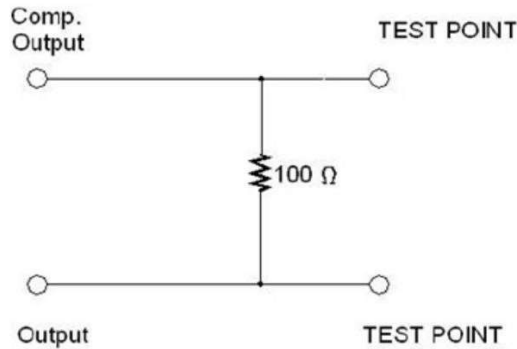
Unit:mm

PAD	Function
#1	Tri-State
#2	N/C
#3	GND
#4	Output
#5	Comp.Output
#6	+VDD

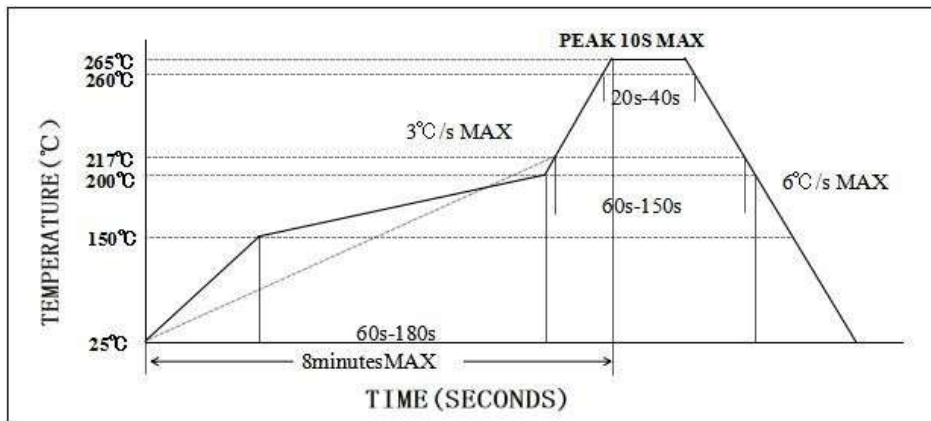
### 2.2 Marking



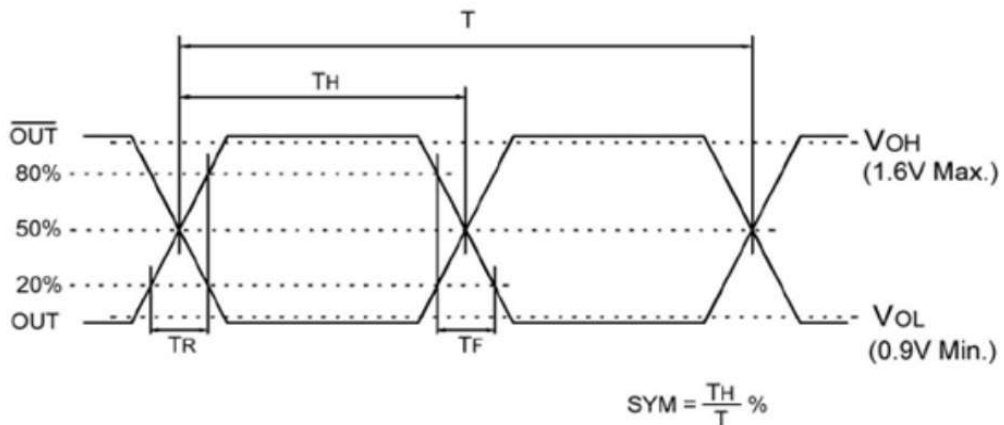
### 3、 Test Circuit



### 4、 Reflow



### 5、 Output Waveform



## 6、 Test Circuit

