

Customer Code :

DATASHEET

DAPU P/N: DPC53125M000AJA2

| Plot | | | The Label |
|------------------|---------|----------|------------------------|
| Drew | Audited | Approved | Stamp, please! Thanks! |
| Jack | David | William | |
| Date: 2023.05.13 | | | |

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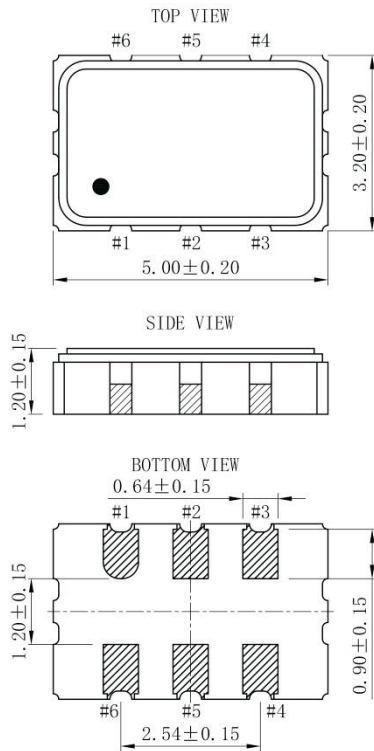
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**1、 Electrical Paramete**

| MODEL: DPC53125M000AJA2 | | | | | | | |
|-------------------------|-----------------------|--|----------------------|------|----------------------|--------------------|---|
| No. | Parameters | SYM. | Electrical Spec. | | | | Notes |
| | | | Min. | Typ. | Max. | Units | |
| 1 | Nominal Frequency | FL | 125.00 | | | MHz | |
| 2 | Supply Voltage | V _{DD} | 3.135 | 3.3 | 3.465 | V | |
| 3 | Frequency Tolerance | - | -20 | | + 20 | × 10 ⁻⁶ | At 25°C |
| 4 | Frequency Stability | - | -25 | | + 25 | × 10 ⁻⁶ | Over Operating Temperature Range (Reference 25°C) |
| 5 | Operating Temperature | T-opr | -55 | ~ | +85 | °C | |
| 6 | Storage Temperature | T-stg | -55 | ~ | +125 | °C | |
| 7 | Current Consumption | I _{DD} /OE | | | 40 | mA | Output Enabled |
| 8 | Output Load Condition | - | 100 | | | Ω | Between outputs |
| 9 | Again | - | -3 | | +3 | × 10 ⁻⁶ | At 25 °C, 3.3 V, First year |
| Enable Feature | | | | | | | |
| 10 | “H” Input Voltage | V _{IH} | V _{DD} *0.7 | | | V | |
| 11 | “L” Input Voltage | V _{IL} | | | V _{DD} *0.3 | V | |
| 12 | E/D(#1 pin E/D) | Logic “1”or Floating ,Outputs enable; Logic “0” ,Outputs disable | | | | | |
| Output LVDS | | | | | | | |
| 13 | “H” Output Voltage | V _{OH} | | | 1.6 | V | |
| 14 | “L” Output Voltage | V _{OL} | 0.9 | | | V | |
| 15 | Output Voltage Swing | V _{OS} | 0.3 | | | V | |
| 16 | Duty Cycle | DC | 45 | | 55 | % | |
| 17 | RMS Jitter | - | | | 200 | fs | 12KHz to 20MHz |



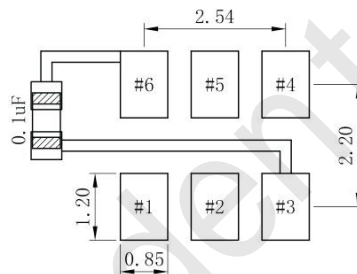
2、 Mechanical Structure



PAD CONNECTIONS

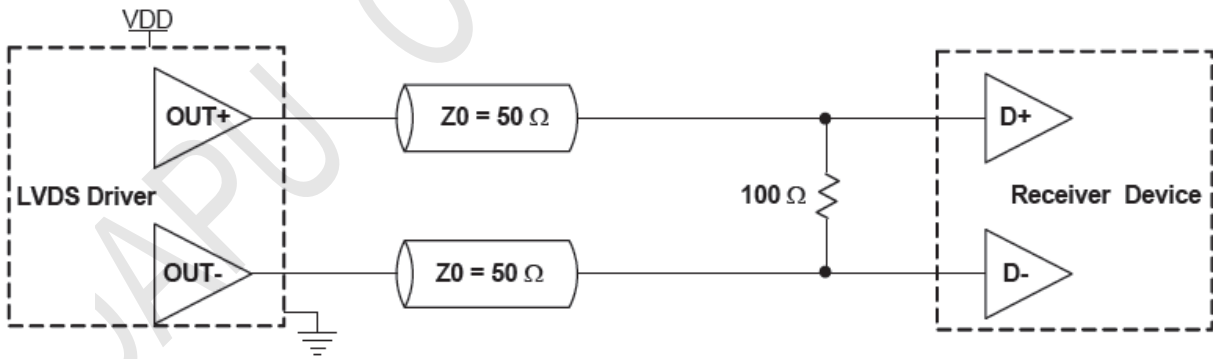
| PAD | FUNCTION |
|-----|----------------------------|
| #1 | Tri-State or N/C |
| #2 | N/C |
| #3 | GND |
| #4 | R. F. Output |
| #5 | Complimentary R. F. Output |
| #6 | +VDC |

RECOMMENDED SOLDER PAD LAYOUT



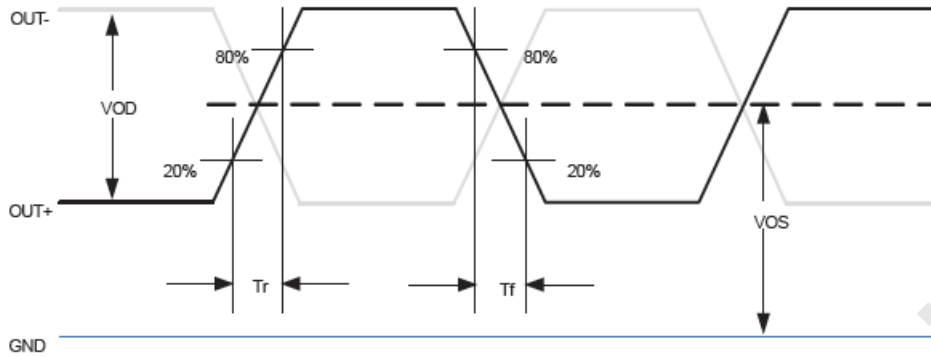
To ensure optimal oscillator performance, place a by-pass capacitor of 0.1uF as close to the part as possible between +VDC and GND pads.

3、 Test Circuit





4、 Output Waveform



5、 Reflow Soldering Curve (RoHS)

