

Travelling Merchant: _____

DATASHEET

Standard: V754-A312-61.44MHz-B

P/N: _____

Plot			The Label
Drew	Audited	Approved	
			Stamp, please! Thanks!
Date: 2020.05.15			

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1. Electrical Parameters

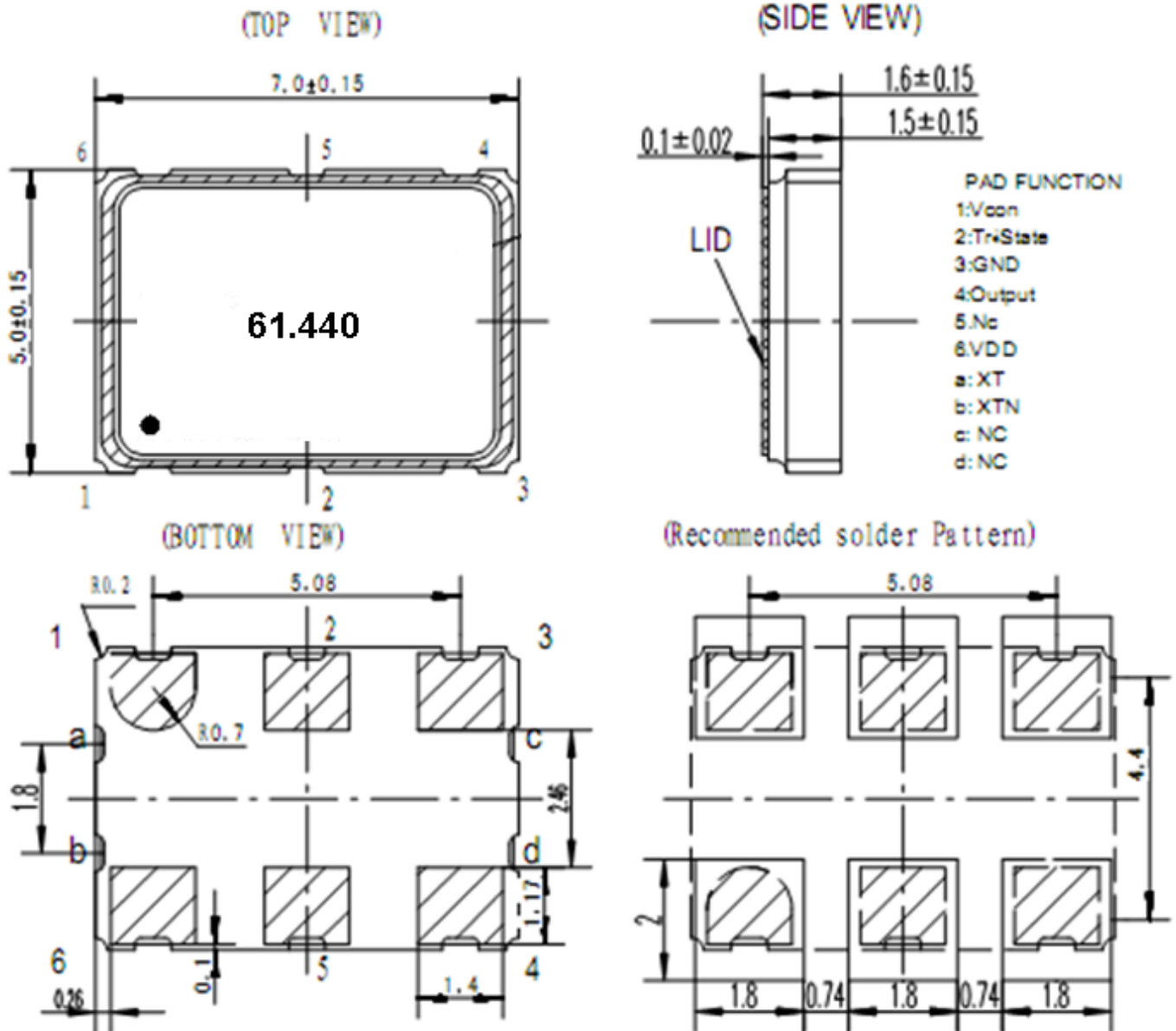
MODEL: V754-A312-61.44MHz-B						
Item	Description	Parameters			Unit	Test Condition
		Min.	Typ.	Max.		
Output	Frequency	61.44			MHz	
	Output Waveform	CMOS				
	Output Low Voltage			0.1V _{DD}	V	V _{cc} =3.3V, O _{load} =15pF
	Output High Voltage	0.9V _{DD}			V	V _{cc} =3.3V, O _{load} =15pF
	Duty Cycle	45	49	55	%	
	Start Time		0.3	1	ms	T ₀ 90% of final amplitude
	Rise / Fall Time (10%~90%)		2.5	5	ns	10%~90% output swing
	Load	15			pF	
Frequency Stabilities	Total Stability	-50		+50	ppm	Frequency tolerance@25 °C and frequency stability vs. operating temperature range and voltage variance and 10 years aging.
	Temperature Stability	-20		+20	ppm	Referenced to T _A =+25 °C.
	Initial Frequency Tolerance	-15		+15	ppm	At time of shipment V _c =1.65V.
	Aging Tolerance 1 Year	-3		+3	ppm	T _A =25 °C, V _{cc} =3.3V, V _c =1.65V and after 1h of operation.
	Aging Tolerance 15 Year	-10		+10	ppm	
Power Supply	Current Consumption		5.5	30	mA	@25 °C, V _{cc} =3.3V, V _c =1.65V, O _{load} =15pF
	Supply Voltage	2.97	3.3	3.63	V	
Voltage Control Characteristics	Frequency Tuning Range	-200		-100	ppm	V _c = 0V. measurement referenced to V _c =1.65V
		-15		+15	ppm	V _c =1.65V. measurement referenced to Exactly 61.44MHz
		+100		+200	ppm	V _c =3.3V. measurement referenced to V _c =1.65V
	Absolute Pull Range	± 50		± 150	ppm	
	Linearity			10	%	
	Input Impedance	2	5		MΩ	



Modulation Band Width	FC	15	20		KHz	
Phase Jitter	RMS		0.3	1	pS	12KHz to 20MHz BW
Phase Noise	Phase Noise @25°C			-60	dBc/Hz	10Hz
				-90		100Hz
				-125		1KHz
				-140		10KHz
				-150		100KHz
				-150		1MHz
Environmental Conditions	Operating Temperature	-40	+25	+85	°C	
	Storage Temperature	-55		+125	°C	
	Vibration	Test Condition: 0.75mm ;acceleration:10g;10Hz~2000Hz, one cycle per 30 min, test 2 hour. (3 times for each 3 directions X , Y , Z).				
	Shock	100g; 6ms; half sine wave (3 times for each 3 directions X ,Y, Z),IEC 68-2-27 Test Ea/Severity 50A.				
	Drop	Test Condition: free drop on steel-made surface or rigid plane from a height of 100cm, IEC 68-2-32.				

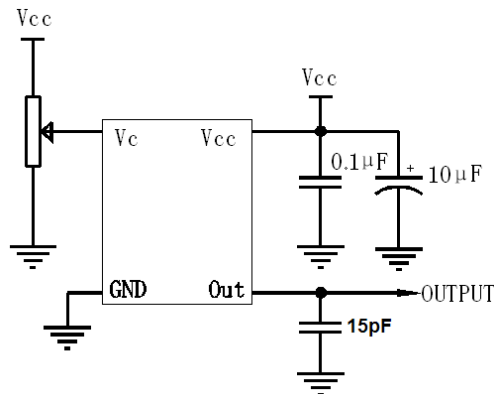


2. Mechanical Structure (mm)

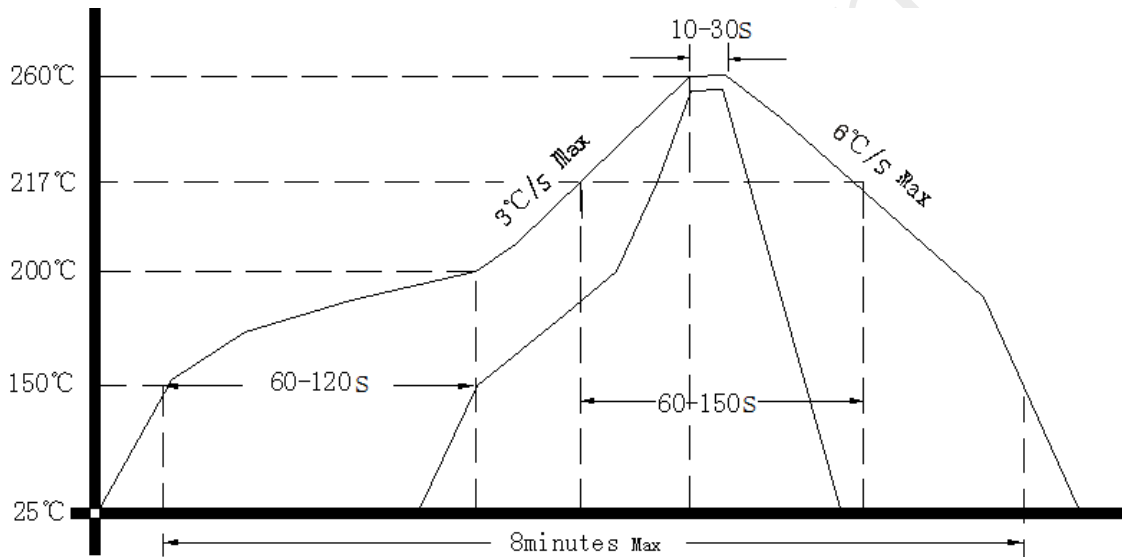




3. Test Circuit



4. Reflow Soldering Curve (RoHS)



5. Package: Tape & Reel (mm)

