

Customer Code : _____

DATASHEET

DAPU P/N: **DP7X4800005**

Plot			The Label
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2019.05.31			

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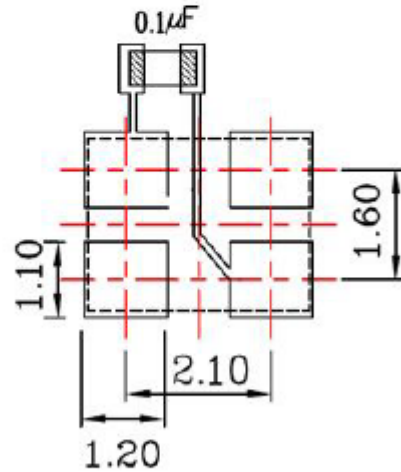
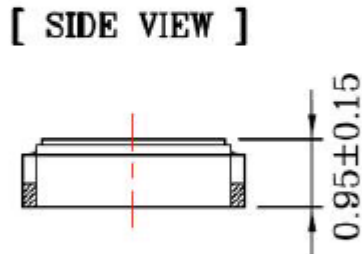
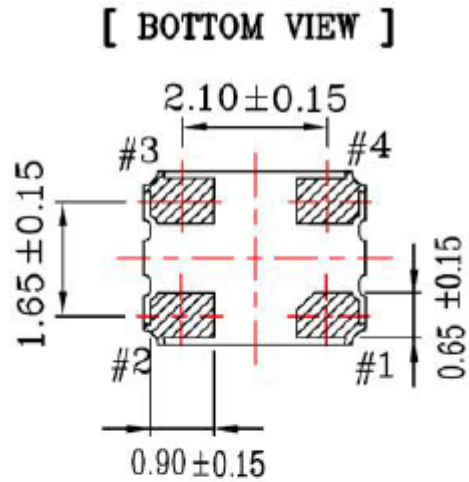
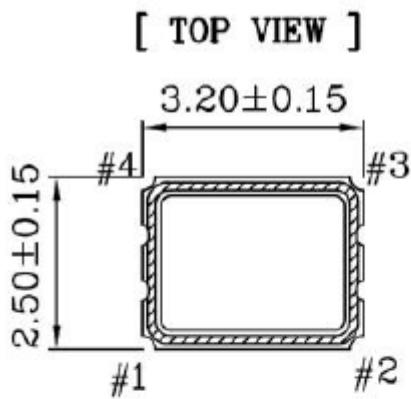
1、Electrical Parameters

MODEL: DP7X4800005							
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL	48.00			MHz	
2	Output Waveform		CMOS				
3	Frequency Stability	-	-50		+50	$\times 10^{-6}$	Frequency stability includes frequency tolerance@25°C and frequency stability vs. operating temperature range and voltage variance and first year aging.
4	Frequency Tolerance vs. Supply voltage		-2		+2	$\times 10^{-6}$	Supply voltage varied $\pm 10\%$ at 25°C
5	Operating Temperature	Topr	-40	~	+85	°C	The operating temperature range over which the frequency stability is measured
6	Storage Temperature	Tstg	-55	~	+125	°C	
7	Supply Voltage	VDD	2.97	3.3	3.63	V	
8	Current	Icc	-	-	15	mA	At maximum supply voltage
9	Output Load	CL			20	pF	
10	Aging	-	-3	-	+3	$\times 10^{-6}/\text{yr}$.	1st. Year at 25°C
11	Output High (Logic "1")	VoH	2.97	-	-	V	
12	Output Low (Logic "0")	Vol	-	-	0.33	V	
13	Duty Cycle	-	45	50	55	%	
14	Start Time				2	ms	
15	Rise Time	Tr	-	-	3	ns	
16	Fall Time	Tf	-	-	3	ns	
17	Output Active		2.31 or Folating			V	Pin 1 Tri-state
18	Output in High-Impedance state				0.99	V	
19	Vibration Test	MIL-STD-883 2007 Condition A				10~2000Hz, 1.52mm, 20g, each axis for 4 hrs	
		JESD22-B103 Condition 1					



20	Thermal Shock	MIL-STD-883 1010 Condition B	-55°C, 125°C; soak time is 10 mins, with total 200 cycles
		JESD22-A104 Condition B	
21	Mechanical Shock	MIL-STD-883 2002 Condition B	1500g half-sine, 0.5ms, each axis for 3 times.
		JESD22-B104 Condition B	

2、Mechanical Structure(mm)



Pin	Function
#1	Tri-State
#2	GND
#3	Output
#4	V _{DD}

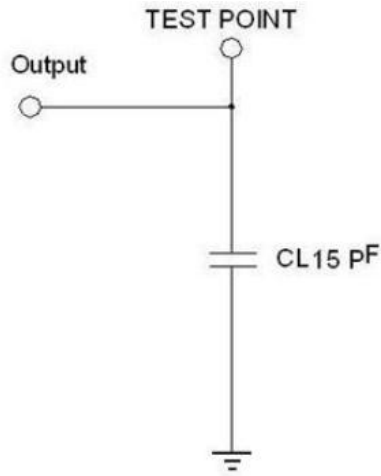
Unit:mm

☆ To ensure optimal oscillator performance. Place a by-pass capacitor of 0.1µF as close to the part as possible between VDD and GND pads.

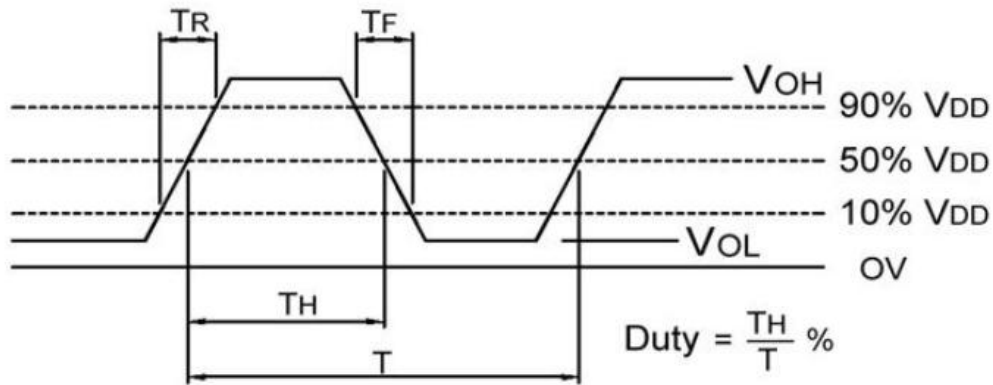
Note1: Tolerance ±0.20mm without mark



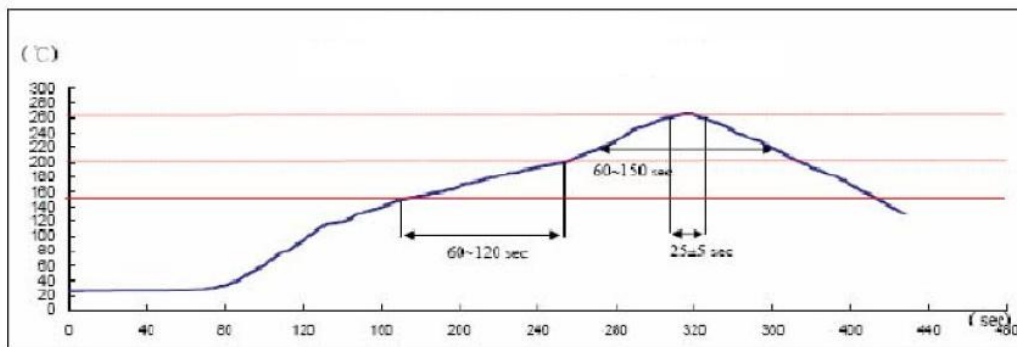
3、 Test Circuit



4、 Output Waveform(CMOS Load)



5、 Recommended Ir Reflow Profile



IR-Reflow Test

Reference Standard : JEDEC-STD 020

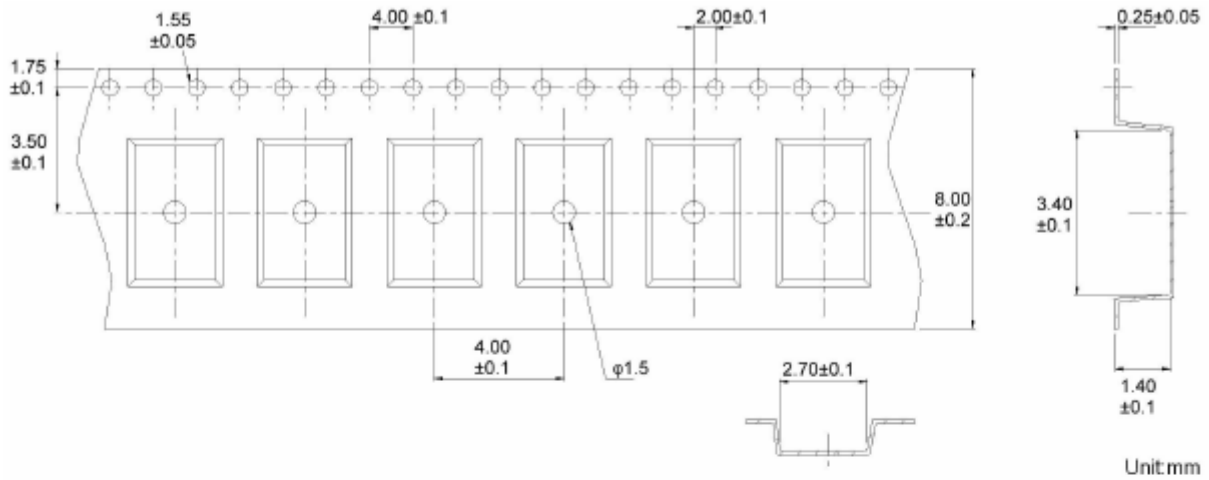
Test Conditions: Pre-heating : 150℃ to 200 ℃, 60~120 sec

Heating : 217 ℃ , 60~150 sec

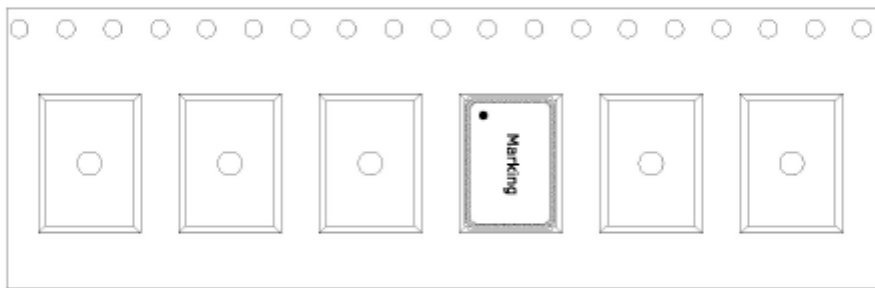
Peak Temperature : 260±5 ℃ , 25±5 sec



6、 Package: Tape & Reel (mm)



➤ THE DIRECTION OF PACKING



➤ REEL DIMENSIONS

