

Customer Code : \_\_\_\_\_

# DATASHEET

DAPU P/N: DP7W15625003

Plot			The Label
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2019.05.14			

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## 1、Electrical Parameters

MODEL: DP7W15625003							
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL	156.250			MHz	
2	Output Waveform		CMOS				
3	Supply Voltage		2.97	3.3	3.63	V	
4	Frequency Stability(Overall)	F-stab	-50		+50	$\times 10^{-6}$	Frequency stability includes frequency tolerance@25°C and frequency stability vs. operating temperature range and voltage variance and first year aging
5	Operating Temperature range	T-use	-20	~	+70	°C	The operating temperature range over which the frequency stability is measured
6	Storage Temperature range		-55		125	°C	
7	Current Consumption	Icc	-		50	mA	At maximum supply voltage
8	Rise/Fall Time	Tr、 Tf			1	ns	
9	Aging		-3		+3	$\times 10^{-6}$	Frequency drift in first year
10	Duty Cycle	DC	45	50	55	%	
11	Start up Time	T_start	-		5	ms	
12	Output Voltage High	VOH	2.97			V	
13	Output Voltage Low	VOL			0.33	V	
14	Output Load				15	pF	
15	Tri-State Output Active		2.31			V	Pin 2 Tri-state Enable High
16	Tri-State Output in High-Impedance state				0.99	V	
17	Center Voltage			1.65		V	
18	Pulling Range		-100		+100	$\times 10^{-6}$	
19	Control Voltage Range		0.3		3.0	V	
20	Absolute Pull Range(APR)		-50		+50	$\times 10^{-6}$	
21	Linearity				10	%	
22	Modulation Bandwidth		10			KHz	
23	VC Input Impedance		1000			Kohm	
24	RMS Phase Jitter				1.5	pS	12KHz-20MHz

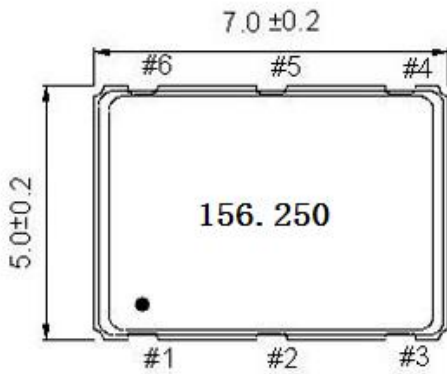


25	Vibration Test	MIL-STD-883 2007 Condition A	10~2000Hz,1.52mm,20g,each axis for 4hrs
		JESD22-B103 Condition 1	
	Thermal shock	MIL-STD-883 1010 Condition B	-55°C, 125°C; soak time is 10 mins, with total 200 cycles
		JESD22-A104 Condition B	
	Mechanical Shock	MIL-STD-883 2002 Condition B	1500g, half-sine, 0.5ms, each axis for 3 times
		JESD22-B104 Condition B	

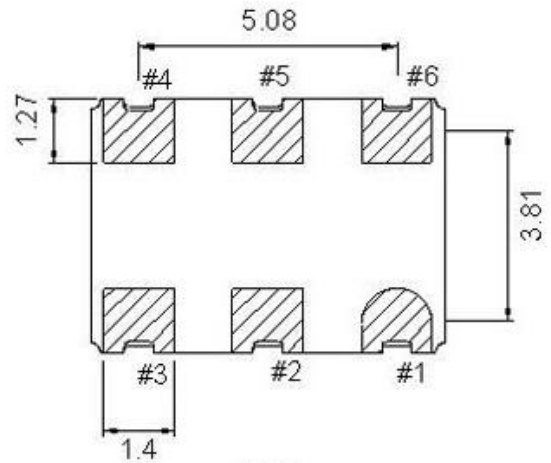
## 2、Mechanical Structure(mm)

Unit:mm

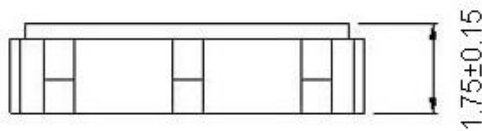
[ TOP VIEW ]



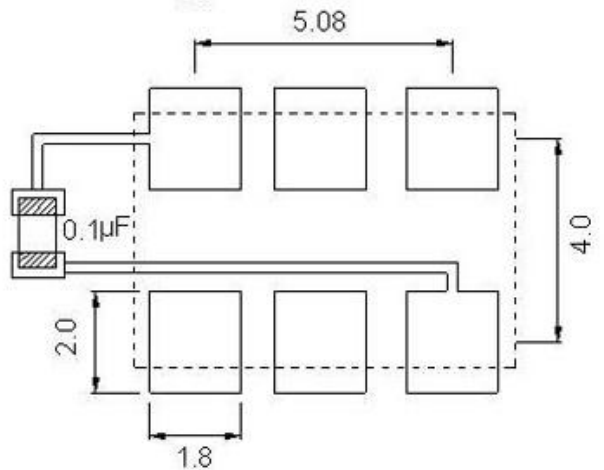
[ BOTTOM VIEW ]



[ SIDE VIEW ]



Pin	Function
#1	Vcon
#2	Tri-State
#3	GND
#4	Output
#5	NC
#6	VDD

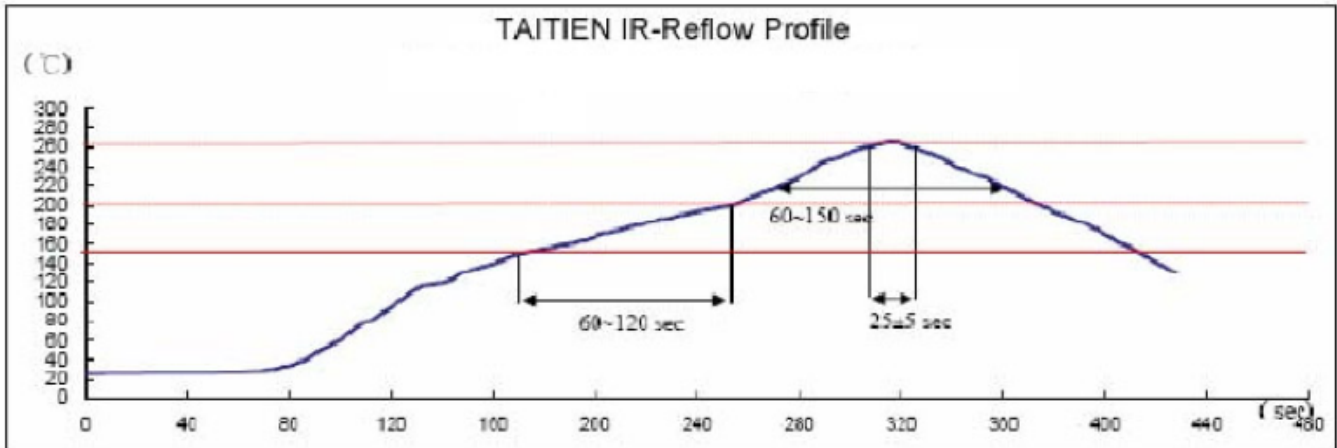


Recommended soldering pattern



### 3、 Recommended IR Reflow Profile

#### ➤ IR REFLOW PROFILE OF CERAMIC SMD PRODUCTS FOR Pb FREE PROCESS



#### IR-Reflow Test

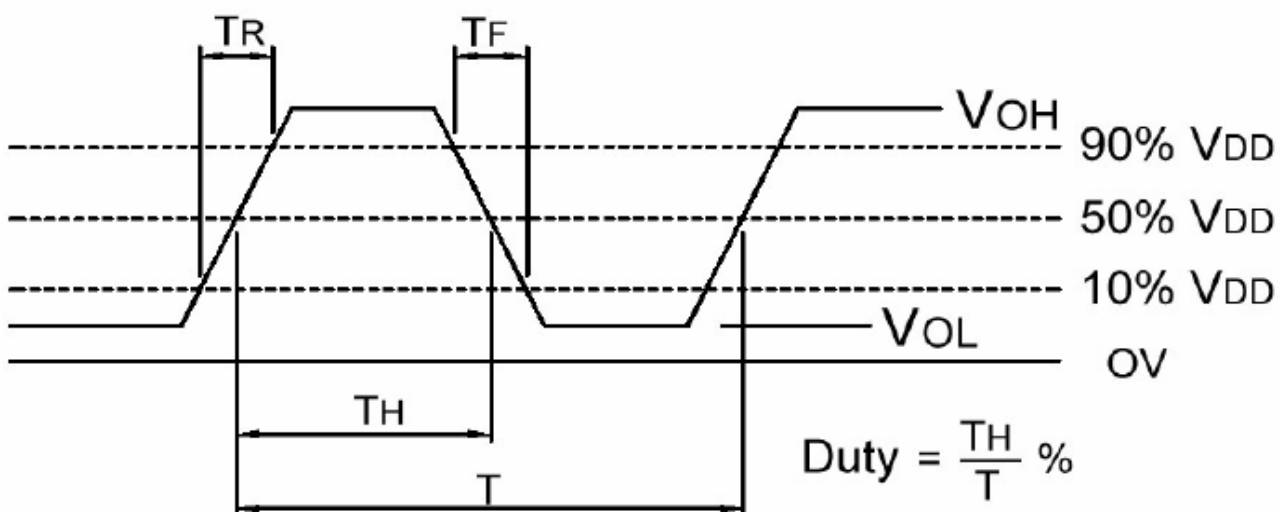
Reference Standard : JEDEC-STD 020

Test Conditions: Pre-heating : 150°C to 200 °C, 60~120 sec

Heating : 217 °C , 60~150 sec

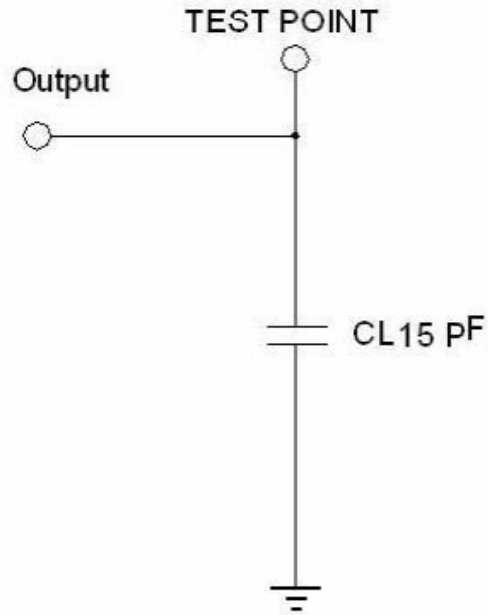
Peak Temperature : 260±5 °C, 25±5 sec

### 4、 Output Waveform(CMOS LOAD)





## 5、 Test Circuit(CMOS LOAD)



## 6、 Package Information

