

Customer Code : _____

DATASHEET

DAPU P/N: **DPBF12500002**

Plot			The Label
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2017.07.13			

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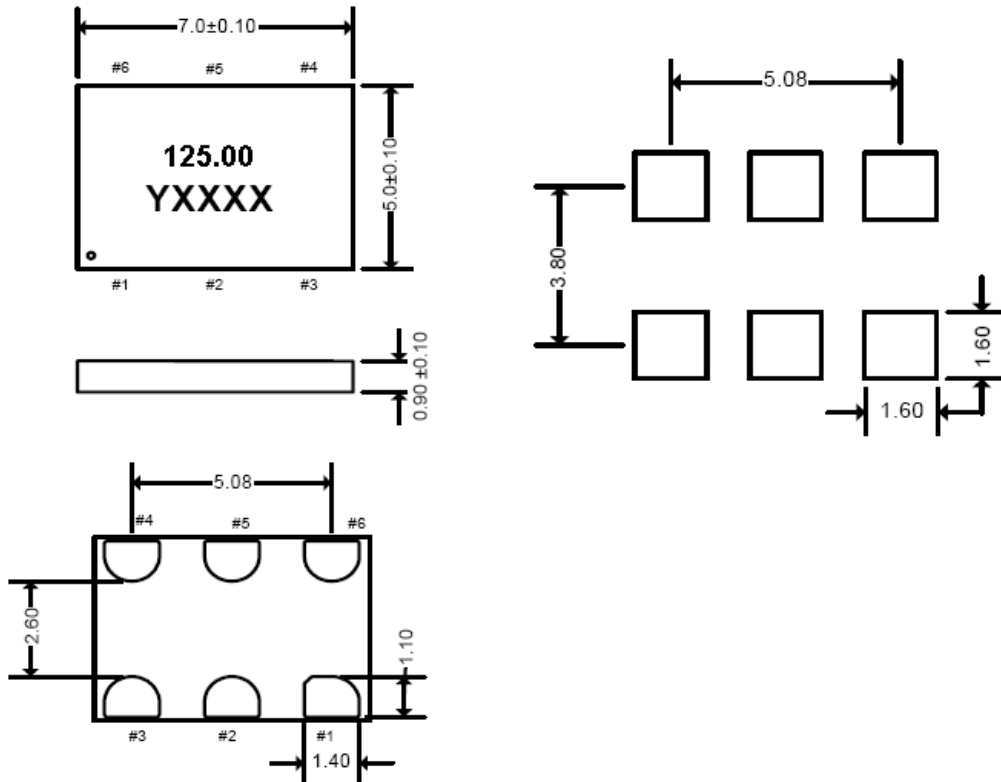
1、Electrical Parameters

MODEL: DPBF12500002							
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL	125.00			MHz	
2	Output Waveform		LVDS				
3	Vdd		-0.5		4	V	
4	Supply Voltage		2.97	3.3	3.63	V	
5	Frequency Stability	F-stab	-25		+25	$\times 10^{-6}$	Inclusive of initial tolerance, operating temperature, rated power supply voltage, and load variations
6	Operating Temperature	T-opr	-40	~	+85	$^{\circ}\text{C}$	
7	Storage Temperature	T-stg	-65	~	+150	$^{\circ}\text{C}$	
8	First Year Aging	F-aging1	-2		+2	$\times 10^{-6}$	25 $^{\circ}\text{C}$
9	10-year Aging	F-aging10	-5		+5	$\times 10^{-6}$	25 $^{\circ}\text{C}$
10	Current Consumption	Idd	-	47	55	mA	
11	OE Disable Current	I_OE			35	mA	OE = Low
12	Standby Current	I_std			100	μA	
13	Rise/Full Time	Tr、Tf		495	600	ps	20%~80%
14	Duty Cycle	DC	45		55	%	
15	Differential Output Voltage	VOD	250	350	450	mV	
16	Output Disable Leakage Current	I_leak			1	μA	
17	VOD Magnitude Change				50	mV	
18	Offset Voltage	VOS	1.125	1.2	1.375	V	
19	VOS Magnitude Change				50	mV	
20	Input Voltage High	VIH	70%	-	-	Vdd	Pin 1
21	Input Voltage Low	VIL	-	-	30%	Vdd	Pin 1
22	Input Pull-up Impedence	Z_in		100	250	K Ω	Pin 1, OE logic high or logic low, or ST logic high
23	Start up Time	T_start	-	6	10	ms	Measured from the time Vdd reaches its rated minimum value
24	OE Enable/Disable Time	T_oe	-	-	115	ns	
25	Resume Time	T_resume		6	10	ms	In Standby mode, measured from the time ST pin crosses 50% threshold.
26	RMS Period Jitter	T_jitt	-	1.2	1.7	ps	
27	Phase Jitter(radom)	T_phj		0.6	0.85	ps	Integration bandwidth =12kHz to 20MHz



28	Mechanical Shock	MIL-STD-883F,Method 2002
	Mechanical Vibration	MIL-STD-883F,Method 2007
	Temperature Cycle	JESD22, Method A104
	Solderability	MIL-STD-883F,Method 2003
	Moisture Sensitivity Level	MSL1 @260°C

2、Mechanical Structure(mm)



unit:mm

Pin Description

Pin	Map	Functionality	
1	\overline{ST}	Input	H or Open: specified frequency output L: Device goes to sleep mode. Supply current reduces to I_{std} .
2	NC	NA	No Connect; Leave it floating or connect to GND for better heat dissipation
3	GND	Power	VDD Power Supply Ground
4	OUT+	Output	Oscillator output
5	OUT-	Output	Complementary oscillator output
6	VDD	Power	Power supply voltage

Note1: Tolerance ± 0.2 mm without mark

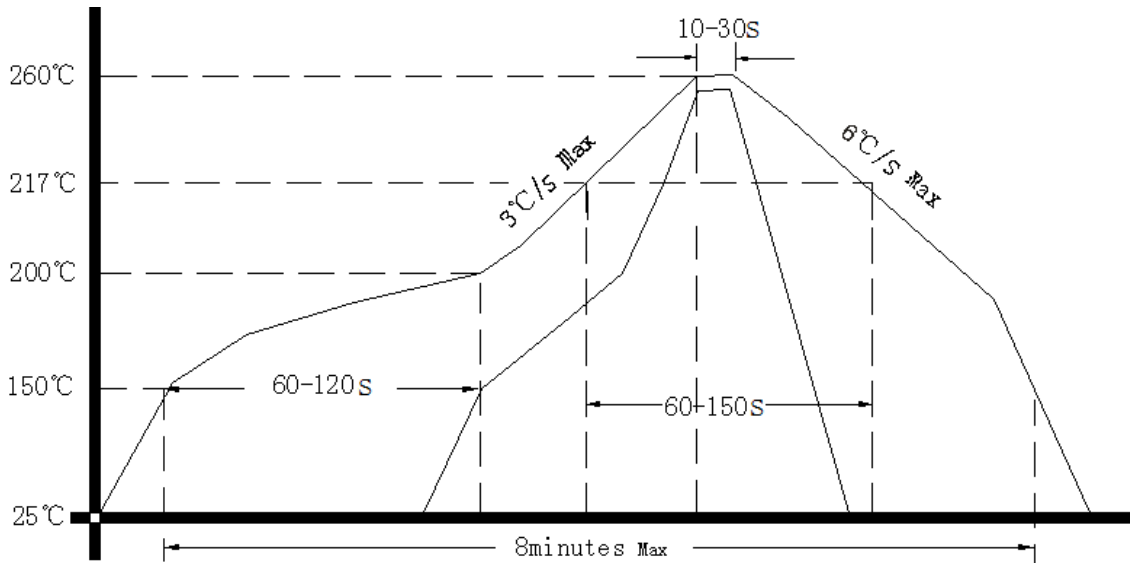
Note2: Referential weight 0.2g

Note3: Y denotes manufacturing origin and XXXX denotes manufacturing lot number. The value of "Y" will depend on the assembly location of the device

Note4: A capacitor of value $0.1\mu F$ or higher between Vdd and GND is required.



3、 Reflow Soldering Curve (RoHS)



4、 Package: Tape & Reel (mm)

