

Customer Code : _____

DATASHEET

DAPU P/N: CM87B-L325-10.00MHz

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DAPU			Customer Approval
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2018.10.18			

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Preliminary



1. General Description

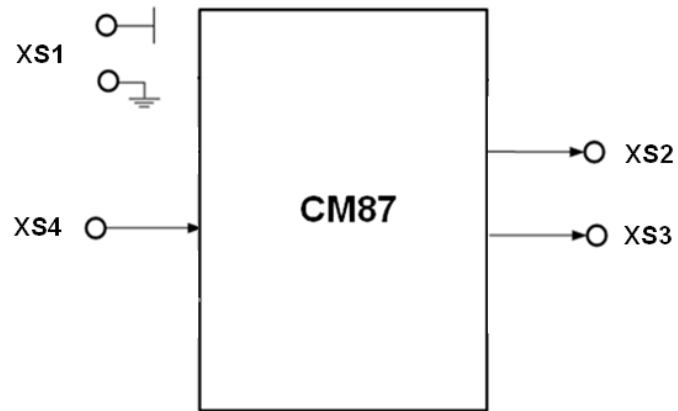


Figure 1 CM87

Figure 1 is the basic diagram of CM87. CM87 is a high-performance clock module designed to provide precise frequency and phase synchronizing with external time reference for telecom and other applications.

Key features:

- **Reference:**10MHz;
- **Clocks Input and Output:** 1*10MHz input, 2*10MHz output;
- **Indicator:** 2*LED
- **Mechanical Size:** 80mm*70mm*26mm.



2. Pin Definition

Table 1 Pin Definition

Pin group	Pin#	Pin Name	Type	Description
Supply Voltage	XS1_Pin 1	VCC	PWR	Power Supply
	XS1_Pin 2			
	XS1_Pin 3	GND	GND	Ground
	XS1_Pin 4			
Output Clocks	XS2	10MHz_OUT	O	10.00MHz Output
	XS3	10MHz_OUT	O	
Input Clock	XS4	10MHz_IN	I	10MHz_IN Reference Input

Table 2 LED Definition

Name	Type	Description
LED1	ON	External 10MHz reference source is valid
	OFF	Others
LED2	ON	CM locked to external 10MHz reference
	OFF	Others

3. Electrical Parameters

Table 3 Electrical Parameters

Parameter	Symbol	Minimum	Typical	Maximum	Units
LVC MOS Input					
High Level Input Voltage	V_{IH}	2.4		3.6	V
Low Level Input Voltage	V_{IL}			0.4	V
LVC MOS Output					
High Level Output Voltage	V_{OH}	2.4			V
Low Level Output Voltage	V_{OL}			0.4	V



4. Performance

Table 4 Performance

Item	Parameter	Minimum	Typical	Maximum	Units	Test Condition	
Clock Output	Nominal Frequency	10.00			MHz	Synchronizing with 10 MHz reference.	
	Duty Cycle	45	50	55	%	Load 15pF	
	Short-term Stability	-0.01		+0.01	$\times 10^{-9}$	V _{CC} =5.0V; T _A =25°C; 1s; using PN9000; no EMI/EMC or other interference.	
	Yearly Aging	-30		+30	$\times 10^{-9}$	V _{CC} , T _A constant measurement referenced to frequency observed with T _A =25°C, V _{CC} =5.0V, in LOCK condition and after 30 days of operation.	
	Ten Years Aging	-50		50	$\times 10^{-9}$		
	Warm up	-50		50	$\times 10^{-9}$	V _{CC} =5.0V; T _A =25°C; constant measurement referenced to 10.00MHz, after power on 5mins with reference lock.	
	Phase Noise			-110	-100	dBc/Hz	10Hz
				-143	-133		100Hz
				-150	-145		1KHz
				-155	-150		10KHz
-155				-150	100KHz		
-160				-155	1MHz		
Supply Voltage	Supply Voltage	4.75	5.0	5.25	V		
	Warm Up Current			1000	mA		
	Steady Current			400	mA	@25°C	
	AC Ripple			50	mV pk-pk	10Hz to 1MHz	



5. Environmental Conditions

Table 5 Environmental Conditions

Parameter	Conditions	
Operating Temperature	-40°C to 70°C	
Storage Temperature	-55°C to 105°C	
Storage Humidity	30%~80%	
ESD Level	Human Body Model, class2: 2000V to 4000V; ANSI/ESDA/JEDEC JS-001-2010.	
	Machine Model, class B: 200V to 400V; ANSI/ESDA/JEDEC JS-001-2010.	
Moisture Sensitivity Level	Not humidity sensitive.	
Vibration	Test Condition: 0.75mm ;acceleration:10g;10Hz~500Hz, one cycle per 30 min, test 2 hour. (3 times for each 3 directions X ,Y , Z), IEC 68-2-06 Test Fc.	
Shock	50g; 11ms; half sine wave (3 times for each 3 directions X, Y, Z), IEC 68-2-27 Test Ea/Severity 50A.	
Relative Humidity	20%~70%	Full Package Storage
Temperature	-10°C~35°C	



6. Mechanical Structure (mm)

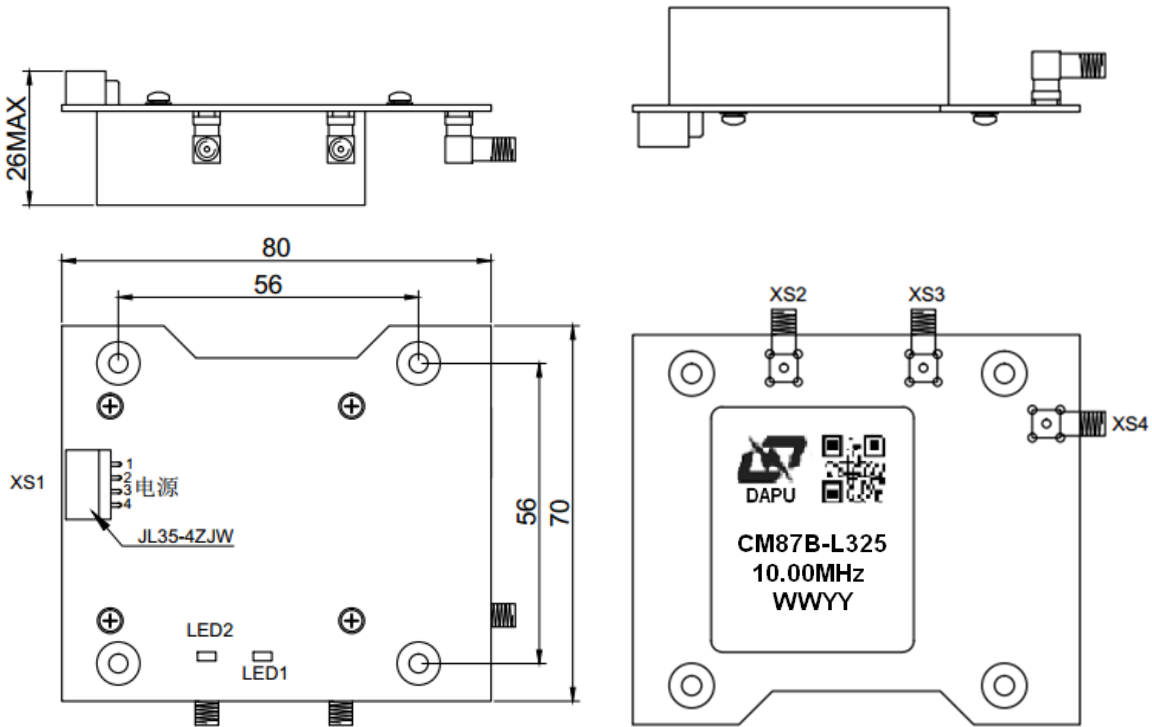


Figure 2 Mechanical structure

- Note1:** Tolerance $\pm 0.3\text{mm}$ without mark.
- Note2:** WW represents Week.
YY represents Year.
- Note3:** XS1 JL35-4ZJW
XS2,XS3,XS4 SMA-KWHD1

7. Package (mm)

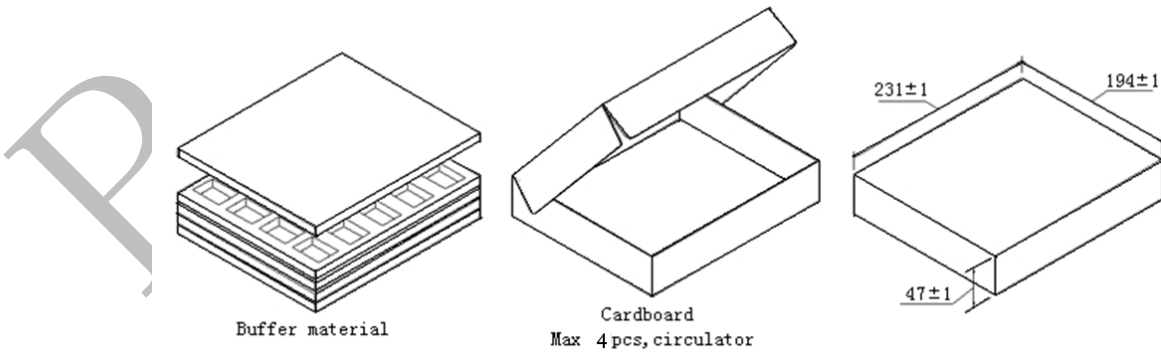


Figure 3 Package