Customer Code :_____

# DATASHEET

DAPU P/N: **DP7X48000004 (Y32NNM48001)**

| Plot | | | The Label |
|---|---|---|---|
| Drew | Audited | Approved | |
| Amway.wei | Carry . Wang | James.Liu | |
| Date: 2015.10.15 | | | Stamp, please! Thanks! |


## Guangdong Dapu Telecom Technology Co.,Ltd

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# 1、Electrical Parameters

| **MODEL：DP7X48000004** | | | | | | | |
|---|---|---|---|---|---|---|---|
| No. | Parameters | SYM. | Electrical Spec. | | | | Notes |
| | | | Min. | Typ. | Max. | Units | |
| 1 | Nominal Frequency | FL | | 48.00 | | MHz | Original frequency |
| 2 | Output Waveform | | | CMOS | | | |
| 3 | Oscillation mode | | | Overtone | | | |
| 4 | Frequency Stability | - | -25 | | +25 | $\times 10^{-6}$ | Incl.25℃tolerance，tolerance over operating temperature range, input Voltage change, load change,1 year aging |
| 5 | Operating Temperature | Topr | -40 | ~ | +85 | ℃ | |
| 6 | Storage Temperature | Tstg | -55 | ~ | +125 | ℃ | |
| 7 | Supply Voltage | VDD | | 3.3±10% | | V | |
| 8 | Input Current | Icc | - | - | 45 | mA | |
| 9 | Output Load： | CL | | 15 | | pF | |
| 10 | Aging | - | -3 | - | +3 | $\times 10^{-6}$/yr. | 1st. Year at 25℃ |
| 11 | Output Voltage High | VoH | 90%Vdd | - | - | V | |
| 12 | Output Voltage Low | Vol | - | - | 10%Vdd | V | |
| 13 | Output Symmetry | - | 40 | 50 | 60 | % | |
| 14 | Rise Time | Tr | - | - | 5 | ns | 10%-90%VDD Level |
| 15 | Fall Time | Tf | - | - | 5 | ns | 90%-10%VDD Level |
| 16 | Start-up Time | Tosc | - | - | 3 | ms | |
| 17 | Standby current | | | | 10 | μ A | |
| 18 | Phase Jitter (rms): | | | | 1 | ps | RMS 12KHz to 20MHz |
| 19 | Output State Control | | | Enable/disable | | | |
| 20 | ESD Level | Human Body Model,class2: 2000V to 4000V; ANSI/ESDA/JEDEC JS-001-2010. | | | | | |
| | | Machine Model, class B: 200V to 400V; ANSI/ESDA/JEDEC JS-001-2010. | | | | | |
| 21 | Moisture Sensitivity Level | Level 2. | | | | | |
| 22 | Vibration | Test Condition: 0.75mm ;acceleration:10g;10Hz～2000Hz, one cycle per 30 min, test 2 hour. (3 times for each 3 directions X ,Y , Z) .IEC 68-2-06 Test Fc. | | | | | |
| 23 | Shock | 100g；6ms；half sine wave (3 times for each 3 directions X ,Y, Z ),IEC 68-2-27 Test Ea/Severity 50A. | | | | | |
| 24 | Full Package Storage | Relative humidity (%) | 20%～70% | | | | |
| | | Temperature (℃) | -10~35℃ | | | | |

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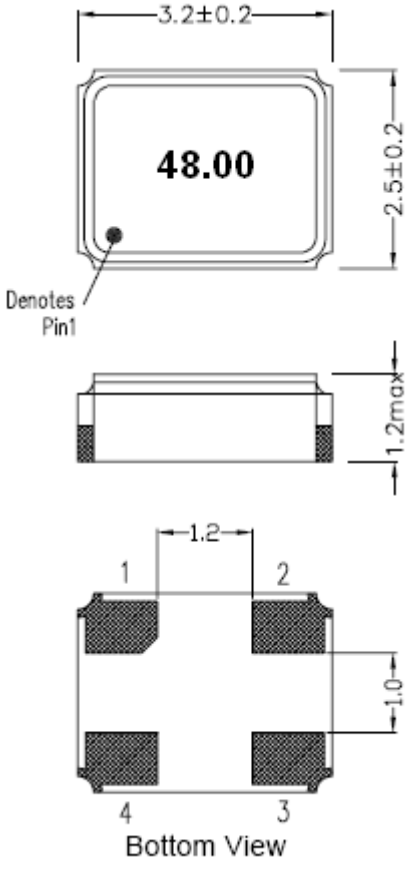
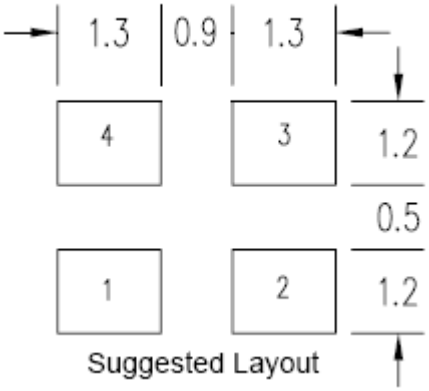
## 2、Mechanical Structure(mm)




PAD FUNCTION:
1: ENABLE CONTROL
2: GND
3: OUT
4: VDD

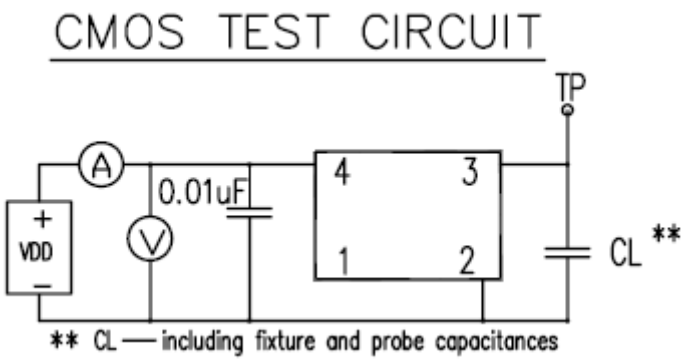



**Note1:** Tolerance ±0.2mm

**Note2:** Enable/disable functional description

When pin1 goes high (≥0.7VDD) or open , the oscillator in normal operation and has output in frequency .When pin1 goes low (≤0.3VDD) , the oscillator stops and the oscillator output (pin3) becomes high impedance.

## 3、Test Diagram



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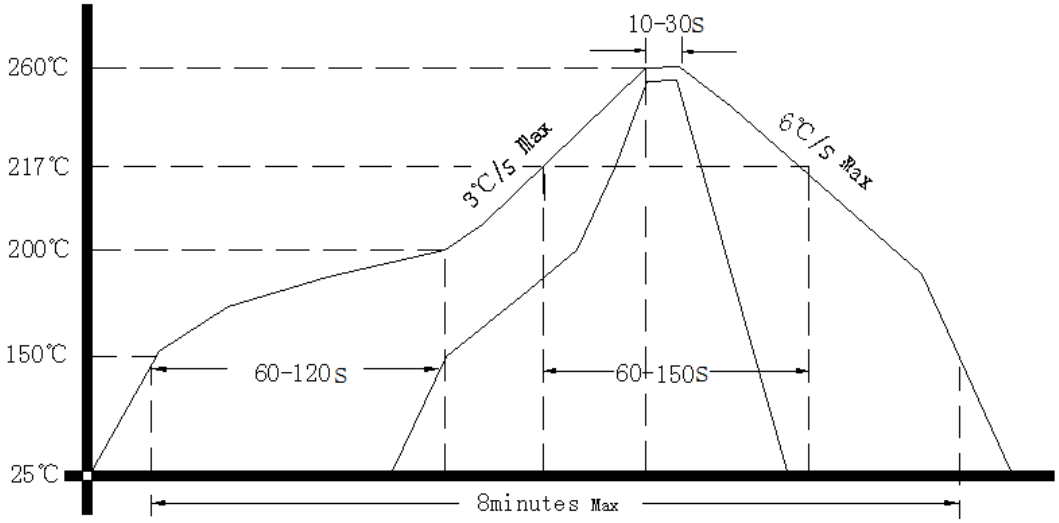
## 4、Reflow Soldering Curve (RoHS)




## 5、Package: Tape & Reel (mm)