Customer Code:_____

DATASHEET

DAPU P/N: **DP8W48000002**

Y22NNM4800A

DAPU			Customer Approval
Drew	Audited	Approved	
Amway.wei	James.Liu	James.Liu	
Date: 2015.07.30			Stamp, please! Thanks!

Guangdong Dapu Telecom Technology Co.,Ltd

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1、Electrical Parameters

MODEL：DP8W48000002							
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL		48.00		MHz	
2	Mode of Oscillation	OT		Overtone			
3	Frequency Stability	-	-50	~	+50	$\times 10^{-6}$	incl. 25℃ tolerance, tolerance over operating temperature range, input voltage change, load change, 1 year aging.
4	Operating Temperature	Topr	-40	~	+85	℃	
5	Storage Temperature	Tstg	-55	~	+125	℃	
6	Supply Voltage	VDD		3.3±10%		V	
7	Input Current	Icc	-	-	45	mA	
8	Output Load：	CL		15		pF	
9	Fan out type	LT		CMOS			
10	Output Voltage High	VoH	2.97	-	-	V	
11	Output Voltage Low	Vol	-	-	0.33	V	
12	Rise Time	Tr	-	-	5	ns	10%-90%VDD Level
13	Fall Time	Tf	-	-	5	ns	90%-10%VDD Level
14	Symmetry（Duty ratio）	TH/T	40	~	60	%	
15	Start-up Time	Tosc	-	-	10	ms	
16	Standby current	-	-	-	10	μ A	
17	Aging	-		±3		$\times 10^{-6}$	1st. Year at 25℃
18	Phase Jitter	-			1	ps	RMS (12kHz to 20MHz)
19	Enable Control	-		Yes		-	PIN1
20	Output Enable	-	0.7VDD			V	
21	Output Disable	-			0.3VDD	V	
22	ESD Level	Human Body Model,class2: 2000V to 4000V; ANSI/ESDA/JEDEC JS-001-2010.					
		Machine Model, class B: 200V to 400V; ANSI/ESDA/JEDEC JS-001-2010.					
23	Moisture Sensitivity Level	Level 2.					
24	Vibration	Test Condition: 0.75mm ;acceleration:10g;10Hz～2000Hz, one cycle per 30 min, test 2 hour. (3 times for each 3 directions X ,Y , Z) .IEC 68-2-06 Test Fc.					
25	Shock	100g；6ms；half sine wave (3 times for each 3 directions X ,Y, Z),IEC 68-2-27 Test Ea/Severity 50A.					
26	Full Package Storage	Relative humidity (%)	20%～70%				
		Temperature (℃)	-10~35℃				

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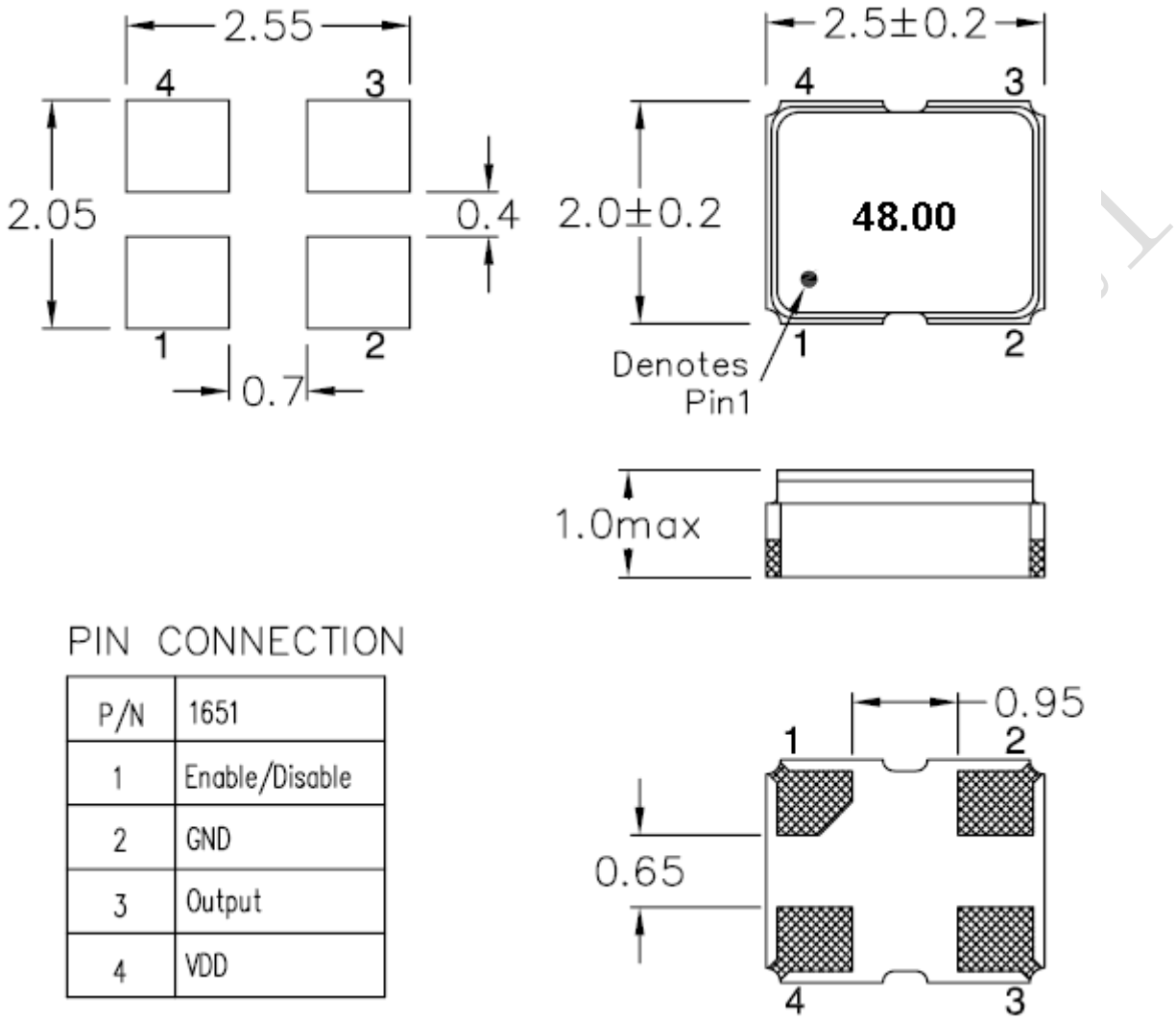
2、Mechanical Structure(mm)



PIN CONNECTION

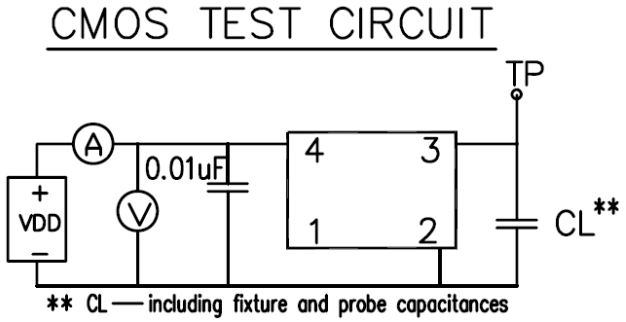
P/N	1651
1	Enable/Disable
2	GND
3	Output
4	VDD

Note1: Tolerance ±0.2mm

Note2: Enable/Disable functional description:

When pin1 goes high (≥0.7VDD) or open, the Oscillator in normal operation and has output in frequency. When pin1 goes low(≤0.3VDD), the Oscillator stops and the Oscillator output (pin3)becomes high impedance.

3、Test Diagram



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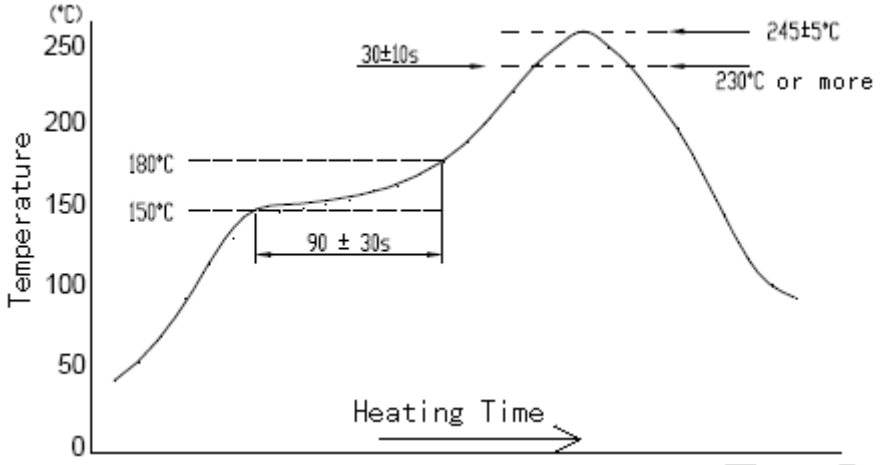
4、Reflow Soldering Curve (RoHS)



5、Package: Tape & Reel (mm)