

Customer Code : _____

DATASHEET

DAPU P/N: **DP7W12500011**

| Plot | | | The Label |
|------------------|---------|----------|------------------------|
| Drew | Audited | Approved | Stamp, please! Thanks! |
| | | | |
| Date: 2018.12.25 | | | |

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1、Electrical Parameters

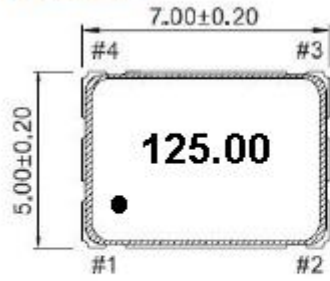
| MODEL: DP7W12500011 | | | | | | | |
|---------------------|--|------------------|------------------|------|------|------------------|---|
| No. | Parameters | SYM. | Electrical Spec. | | | | Notes |
| | | | Min. | Typ. | Max. | Units | |
| 1 | Nominal Frequency | FL | 125.00 | | | MHz | |
| 2 | Output Waveform | | CMOS | | | | |
| 3 | Supply Voltage | | 2.97 | 3.3 | 3.63 | V | |
| 4 | Frequency Stability(Overall) | F-stab | -50 | | +50 | $\times 10^{-6}$ | Frequency stability includes frequency tolerance@25°C and frequency stability vs. operating temperature range and voltage variance and first year aging |
| 5 | Operating Temperature range | T-use | -40 | ~ | +85 | °C | The operating temperature range over which the frequency stability is measured |
| 6 | Storage Temperature range | | -55 | | 125 | °C | |
| 7 | Current Consumption | Icc | - | | 35 | mA | At maximum supply voltage |
| 8 | Rise/Fall Time | Tr、 Tf | | | 3 | ns | |
| 9 | Aging | | -3 | | +3 | $\times 10^{-6}$ | Frequency drift in first year |
| 10 | Duty Cycle | DC | 45 | 50 | 55 | % | |
| 11 | Start up Time | T_start | - | | 5 | ms | |
| 12 | Output Voltage High | VOH | 2.97 | | | V | |
| 13 | Output Voltage Low | VOL | | | 0.33 | V | |
| 14 | Output Load | | | | 15 | pF | |
| 15 | Tri-State Output Active | 2.31 or Floating | | | | V | Pin 1 Tri-state |
| 16 | Tri-State Output in High-Impedance state | | | | 0.99 | V | |

| | | | | |
|----|------------------|------------------------------|---|---|
| 17 | Vibration Test | MIL-STD-883 2007 Condition A | 10~2000Hz,1.52mm,20g,each axis for 4hrs | |
| | | JESD22-B103 Condition 1 | | |
| | Thermal shock | MIL-STD-883 1010 Condition B | | -55°C, 125°C; soak time is 10 mins, with total 200 cycles |
| | | JESD22-A104 Condition B | | |
| | Mechanical Shock | MIL-STD-883 2002 Condition B | | 1500g, half-sine, 0.5ms, each axis for 3 times |
| | | JESD22-B104 Condition B | | |

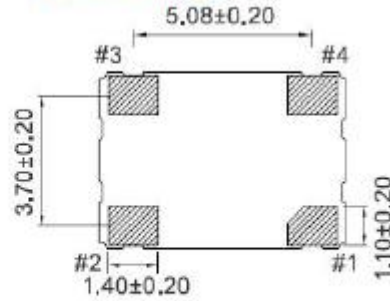


2、 Mechanical Structure(mm)

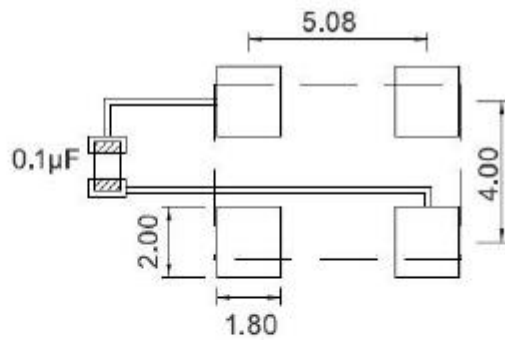
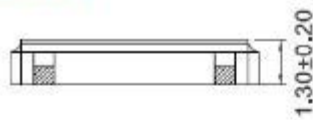
[TOP VIEW]



[BOTTOM VIEW]



[SIDE VIEW]



PIN FUNCTIONS

| Pin | Function |
|-----|-----------------|
| #1 | Tri-State |
| #2 | GND |
| #3 | Output |
| #4 | V _{DD} |

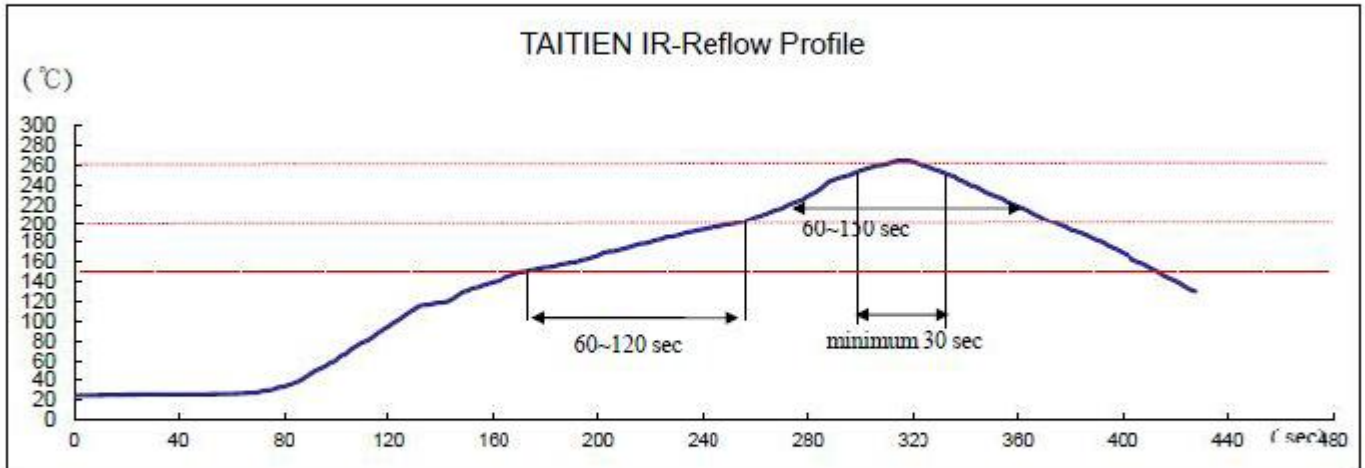
To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between V_{DD} and GND pads.



3、 Recommended IR Reflow Profile

➤ IR REFLOW PROFILE OF CERAMIC SMD PRODUCTS FOR Pb FREE PROCESS

TAITIEN ELECTRONICS CO., LTD.



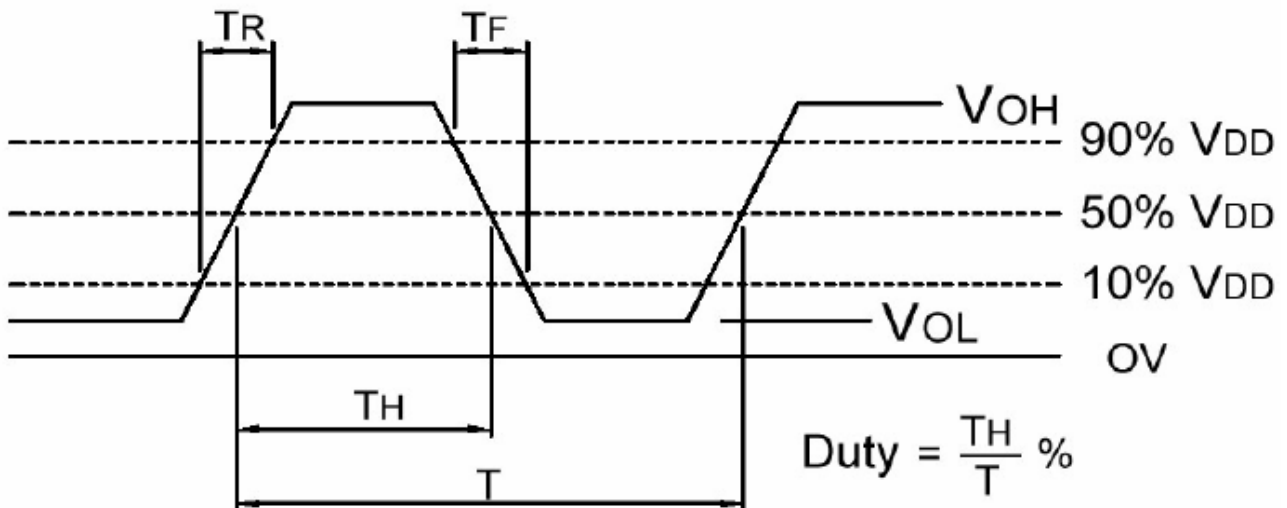
Reference Standard: JEDEC-STD 020

Test conditions: Pre-heating : 150°C to 200°C, 60~120secs.

Heating : 217°C, 60~150sec.

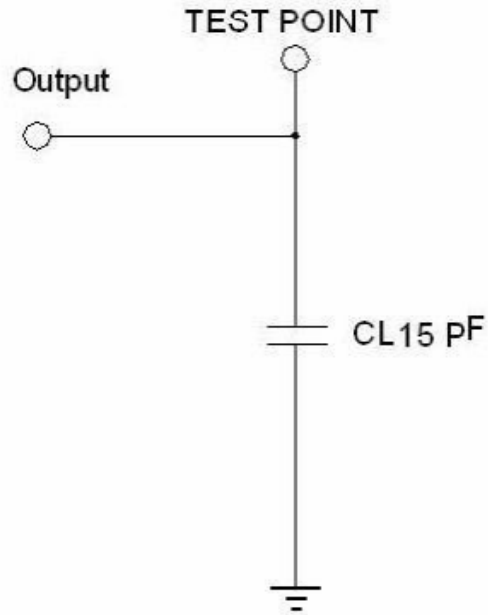
Peak temperature at least : 260°C, The time above 255 °C : minimum 30sec.

4、 Output Waveform(CMOS LOAD)





5、 Test Circuit(CMOS LOAD)



6、 Package Information

