Customer Code :_____

DATASHEET

DAPU P/N: **DP8Y30000001**

Plot			The Label
Drew	Audited	Approved	
Amway.wei	Carry.Wang	James.Liu	
Date: 2018.12.14			Stamp, please! Thanks!

Guangdong Dapu Telecom Technology Co.,Ltd

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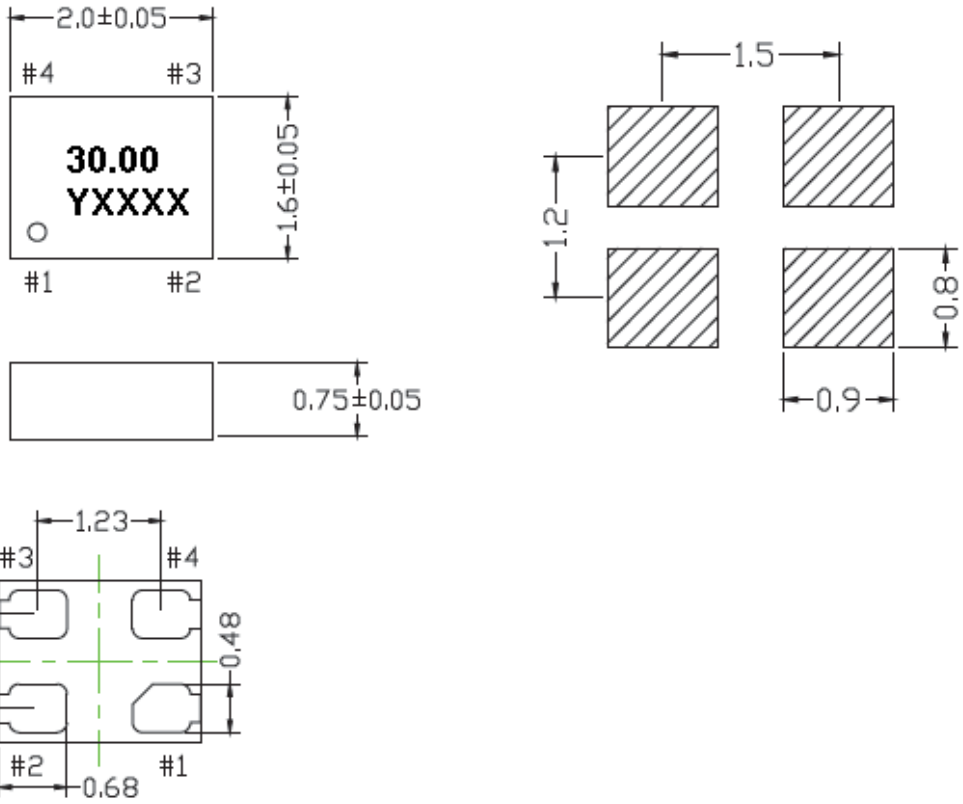
1、Electrical Parameters

MODEL：DP8Y30000001							
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL		30.00		MHz	
2	Output Waveform			LVCMOS			
3	Vdd		-0.5		4	V	
4	Supply Voltage		1.71	1.8	1.89	V	
5	Frequency Stability	F-stab	-25		+25	$\times 10^{-6}$	Inclusive of initial tolerance at 25℃, 1st year aging at 25 ℃,and variations Over operating temperature, rated power supply voltage, and load .
6	Operating Temperature	T-opr	-40	~	+85	℃	
7	Storage Temperature	T-stg	-65	~	+150	℃	
8	Current Consumption	Idd	-	3.8	4.5	mA	
9	OE Disable Current	I_OD			4.2	mA	
10	Standby Current	I_std		2.1	4.3	μA	
11	Rise/Full Time	Tr、Tf		1	2	ns	20%~80%
12	Duty Cycle	DC	45		55	%	
13	Output Voltage High	VOH	90%	-		Vdd	
14	Output Voltage Low	VOL		-	10%	Vdd	
15	Input Voltage High	VIH	70%	-	-	Vdd	Pin 1
16	Input Voltage Low	VIL	-	-	30%	Vdd	Pin 1
17	Input Pull-up Impedence	Z_in	50	87	150	KΩ	Pin 1, OE logic high or logic low, or ST logic high
18	Start up Time	T_start	-		5	ms	Measured from the time Vdd reaches its rated minimum value
19	Enable/Disable Time	T_oe	-	-	130	ns	
20	Resume Time	T_ resume			5	ms	In Standby mode, measured from the time ST pin crosses 50% threshold.
21	RMS Period Jitter	T_jitt	-	1.8	3	ps	
22	Peak to peak Period Jitter	T_pk		12	25	ps	
23	Phase Jitter(radom)	T_phj		0.5	0.9	ps	Integration bandwidth =900kHz to 7.5MHz
				1.3	2	ps	Integration bandwidth =12kHz to 20MHz

24	Mechanical Shock	MIL-STD-883F,Method 2002
	Mechanical Vibration	MIL-STD-883F,Method 2007
	Temperature Cycle	JESD22, Method A104
	Solderability	MIL-STD-883F,Method 2003
	Moisture Sensitivity Level	MSL1@260℃

2、Mechanical Structure(mm)



Pin Description

Pin	Symbol	Functionality	
1	NC	No Connect	Any voltage between 0 and Vdd or Open[1]: Specified frequency output. Pin 1 has no function.
2	GND	Power	Electrical ground
3	OUT	Output	Oscillator output
4	VDD	Power	Power supply voltage[2]

Note1: Tolerance±0.2mm without mark

Note2: Referential weight 0.2g

Note3: Y denotes manufacturing origin and XXXX denotes manufacturing lot number. The value of "Y" will depend on the assembly location of the device

Note4: A capacitor of value 0.1μ F or higher between Vdd and GND is required.

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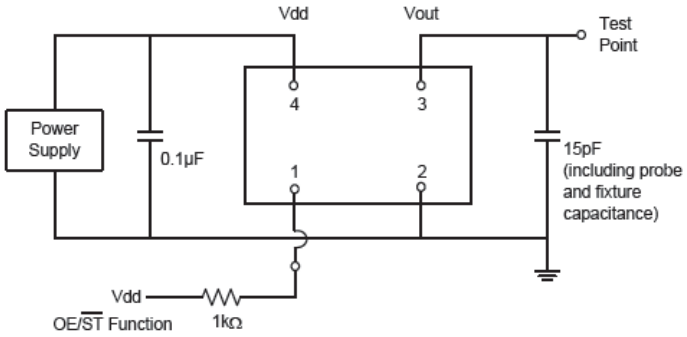
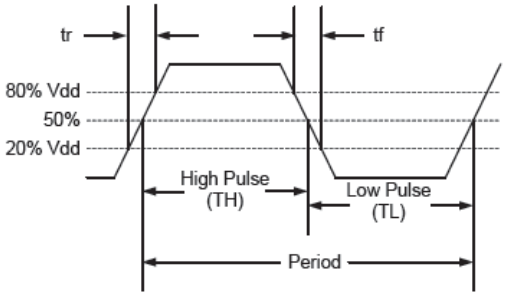
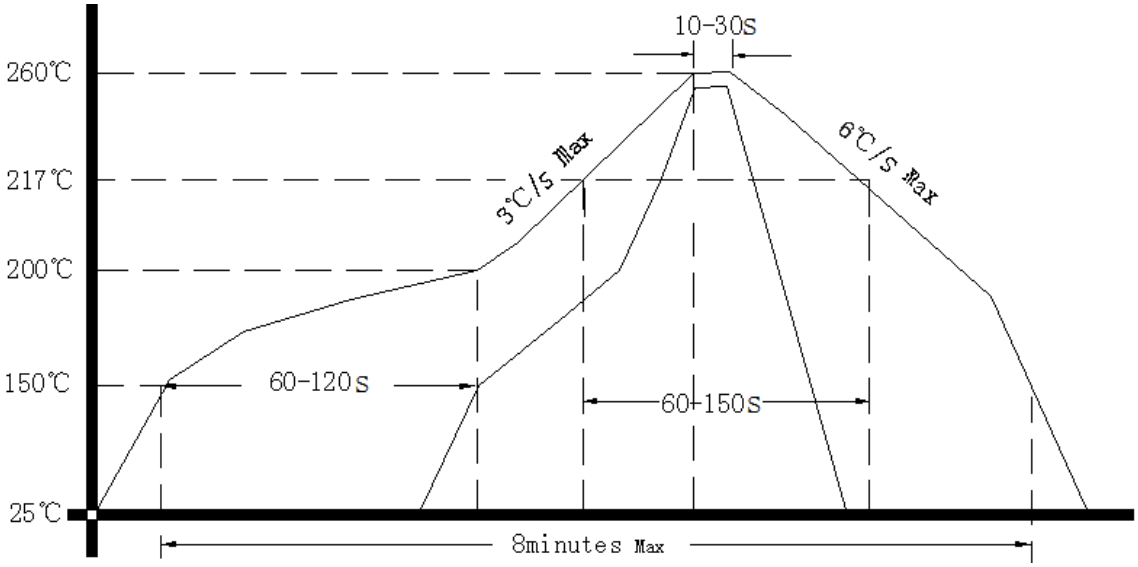
3、Test Circuit and Waveform





4、Reflow Soldering Curve (RoHS)



5、Package: Tape & Reel (mm)