

Travelling Merchant: _____

DATASHEET

Standard: **T75B-F519-40.00MHz-B**

P/N: _____

Plot			The Label
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2020.01.17			

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1. Electrical Parameters

MODEL: T75B-F519-40.00MHz-B						
Item	Description	Parameters			Unit	Test Condition
		Min.	Typ.	Max.		
Output	Frequency	40.00			MHz	
	Output Waveform	Clipped Sine Wave				
	V _{p-p}	0.8			V	
	Load	10KΩ//10pF				
Frequency Stabilities	Frequency Tolerance vs. Operating Temperature Range	-0.5		+0.5	× 10 ⁻⁶	T _A varied from -40 to 85°C, measurement referenced to frequency observed with f _{ref} =(f _{max} +f _{min})/2, V _{cc} =3.3V, O _{load} =10KΩ//10pF.
	Initial Frequency Tolerance	-1		+1	× 10 ⁻⁶	Measurement referenced to frequency observed with T _A =25°C ±2°C, 10% load change, V _{cc} varied from 3.13V to 3.47V, within 30 days after ex-works.
	Frequency Tolerance vs. Supply Voltage	-0.2		+0.2	× 10 ⁻⁶	Measurement referenced to frequency observed T _A =25°C, V _{cc} varied from 3.13V to 3.47V, and O _{Load} =10KΩ//10pF.
	Frequency Tolerance vs. Load	-0.2		+0.2	× 10 ⁻⁶	10% load change measurement referenced to frequency observed with T _A =25°C, V _{cc} =3.3V, O _{Load} =10KΩ//10pF.
	Aging Tolerance Per Day	-0.02		+0.02	× 10 ⁻⁶	T _A =25°C, V _{cc} =3.3V, and after 1h of operation.
	Aging Tolerance 1 Year	-1		+1	× 10 ⁻⁶	
Power Supply	Current Consumption			10	mA	@25°C, V _{cc} =3.3V, O _{load} =10KΩ//10pF.
	Supply Voltage	3.13	3.3	3.47	V	
Phase Noise	Phase Noise		-140		dBc/Hz	1KHz

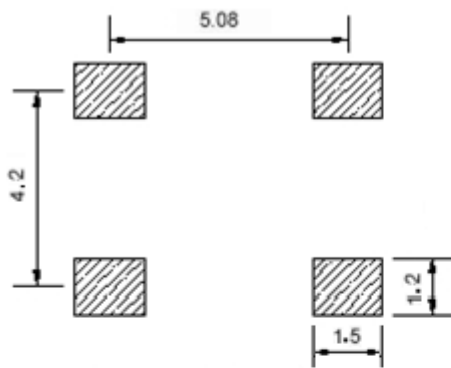


Environmental Conditions	Operable Temperature	-40		+85	°C	
	Storage Temperature	-55		+105	°C	
	ESD Level	Human Body Model, class2: 2000V to 4000V; ANSI/ESDA/JEDEC JS-001-2010.				
		Machine Model, class B: 200V to 400V; ANSI/ESDA/JEDEC JS-001-2010.				
	Moisture Sensitivity Level	Level 3.				
	Vibration	Test Condition: 0.75mm ;acceleration:10g;10Hz~2000Hz, one cycle per 30 min, test 2 hours. (3 times for each 3 directions X , Y , Z) .IEC 68-2-06 Test Fc.				
Shock	100g; 6ms; half sine wave (3 times for each 3 directions X , Y, Z),IEC 68-2-27 Test Ea/Severity 50A.					
Full Package Storage	Relative humidity (%)	20% ~ 70%				
	Temperature (°C)	-10~35°C				

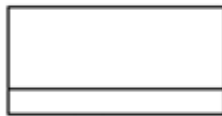
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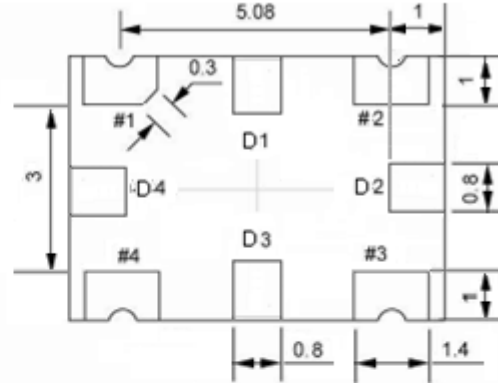
2. Mechanical Structure(mm)



Solder pad layout



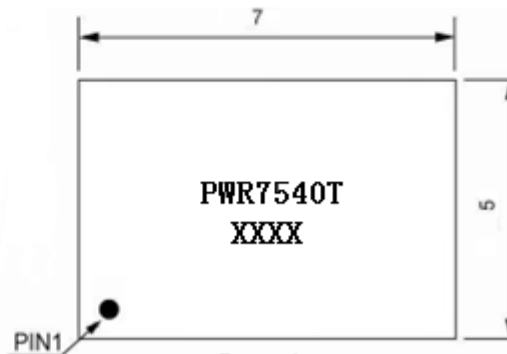
Right view



Bottom view



Side view



Top view

PIN FUNCTION

PIN	NOTATION	FUNCTION
D1, D2, D3, D4	NC	Not Connect
1	NC	Not Connect
2	GND	GND
3	OUTPUT	RF Output
4	VCC	Supply Voltage

Note1: Tolerance $\pm 0.2\text{mm}$ without mark

Note2: The first two xx representative: year

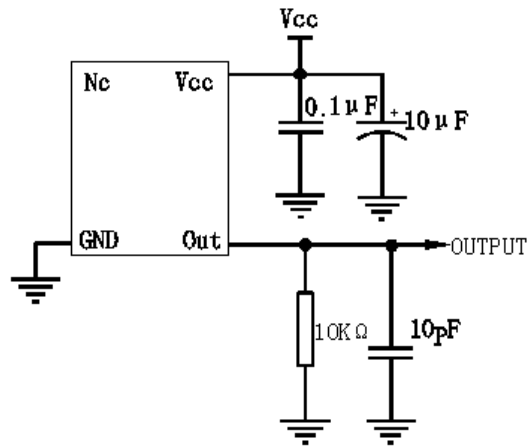
After two xx representative: week

Note3: Referential weight 0.2g

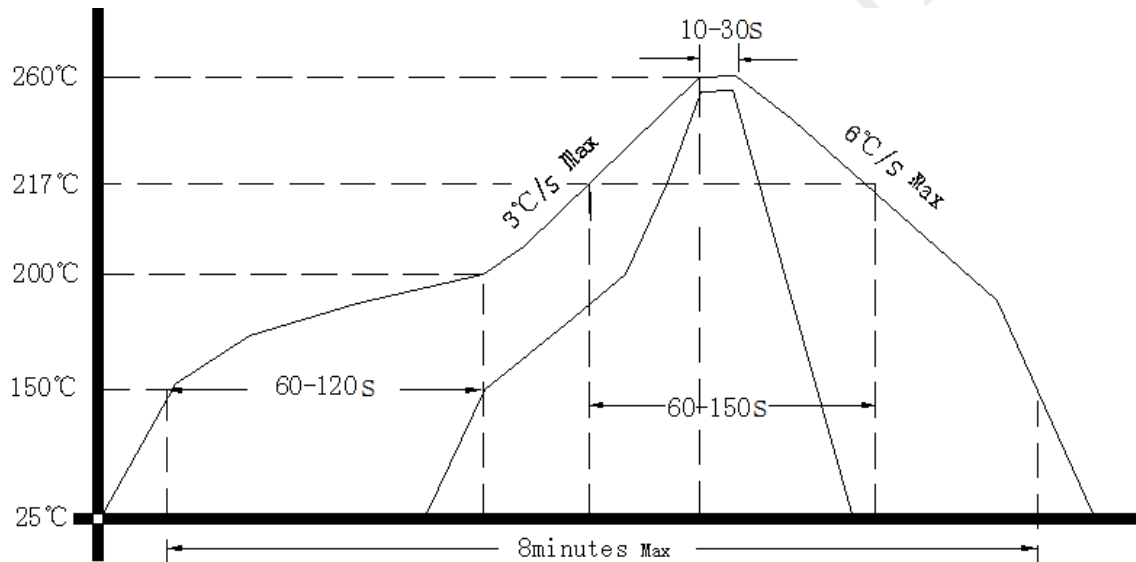
Note4: NC is not connect



3. Test circuit



4. Reflow Soldering Curve (RoHS)



5. Package: Tape & Reel (mm)

