

Customer Code: \_\_\_\_\_

# DATASHEET

DAPU P/N: OSC324-D106-KD-8.00MHz

| DAPU             |         |          | Customer Approval      |
|------------------|---------|----------|------------------------|
| Drew             | Audited | Approved | Stamp, please! Thanks! |
|                  |         |          |                        |
| Date: 2023.10.23 |         |          |                        |

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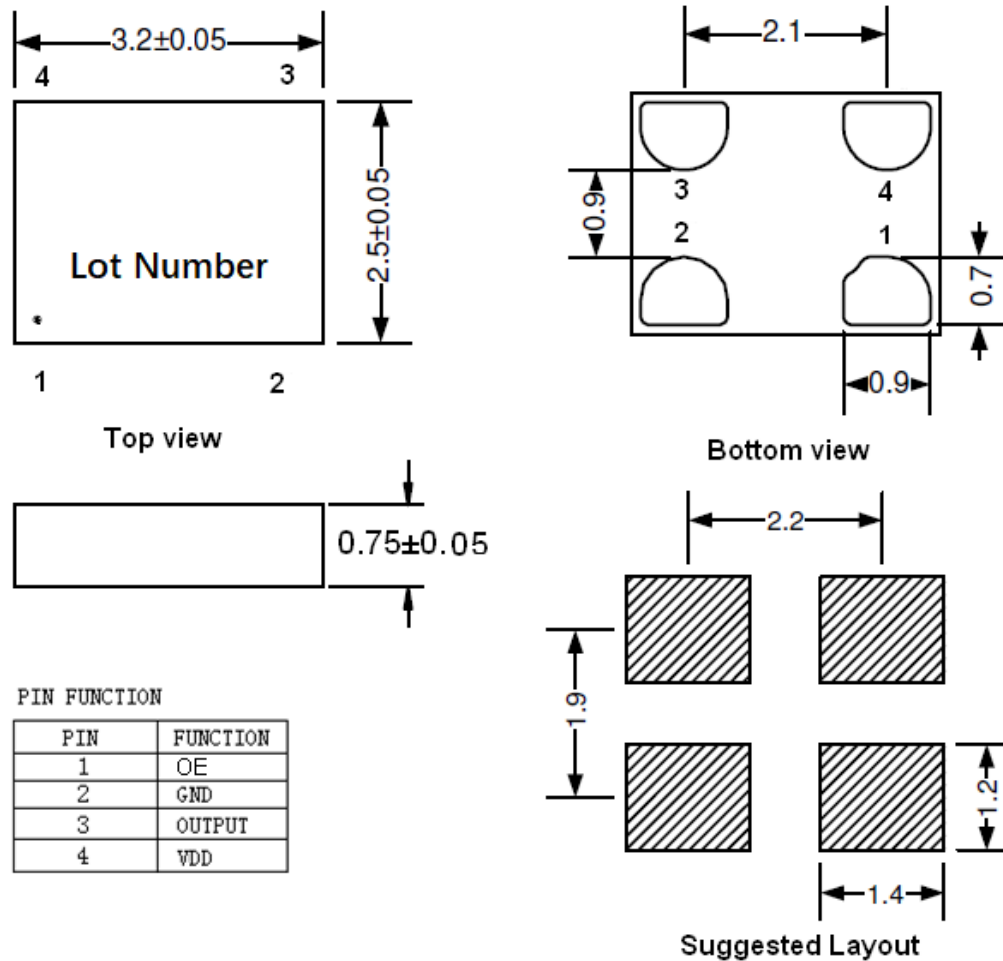
## 1、 Electrical Parameters

| MODEL: OSC324-D106-KD-8.00MHz |                          |  |                  |      |            |                    |  |
|-------------------------------|--------------------------|--|------------------|------|------------|--------------------|--|
| No.                           | Parameters               | SYM.                                     | Electrical Spec. |      |            |                    | Notes  |
|                               |                          |  | Min.             | Typ. | Max.       | Units              |  |
| 1                             | Nominal Frequency        | FL                                       | 8.00             |      |            | MHz                |  |
| 2                             | Frequency Stability      | -  | -20              | ~    | +20        | $\times 10^{-6}$   |  |
| 3                             | Operating Temperature    | Topr                                     | -40              | ~    | 85         | $^{\circ}\text{C}$ |  |
| 4                             | Storage Temperature      | Tstg                                     | -55              | ~    | 125        | $^{\circ}\text{C}$ |  |
| 5                             | Supply Voltage           | VDD                                      | 2.7              | 3.0  | 3.3        | V                  |  |
| 6                             | Input Current            | Icc                                      | -                | 3.8  | 4.5        | mA                 |  |
| 7                             | Output waveform          |  | HCMOS            |      |            |                    |  |
| 8                             | Output Load:             | CL                                       | 15               |      |            | pF                 |  |
| 9                             | Output Voltage High      | VOH                                      | 90%              | -    | -          | Vdd                |  |
| 10                            | Output Voltage Low       | VOL                                      | -                | -    | 10%        | Vdd                |  |
| 11                            | Rise/Fall Time           | Tr、 Tf                                   | -                | 1.0  | 2.0        | ns                 | 20%-80% VDD Level  |
| 12                            | Aging                    | -  | -1.0             | ~    | 1.0        | $\times 10^{-6}$   | 1st. Year at 25 $^{\circ}\text{C}$                               |
| 13                            | Tri-State Output Enable  |  | 70%<br>Vdd       | -    | -          | V                  | Pin 1,OE   |
| 14                            | Tri-State Output Disable |  | -                | -    | 30%<br>Vdd | V                  | Pin 1,OE   |
| 15                            | OE Disable Current       | I_OD                                     | -                | -    | 4          | mA                 |  |
| 16                            | Standby Current          | I_std                                    | -                | 2.6  | 4.3        | $\mu\text{A}$      |  |
| 17                            | Duty Cycle               | -  | 45               | ~    | 55         | %                  |  |
| 18                            | Phase Jitter             | T_phj                                    | -                | 0.5  | 0.9        | ps                 | 900 kHz~7.5 MHz  |
| 19                            | Start-Up Time            | T_start                                  | -                | -    | 5          | ms                 | Measured from the time<br>Vdd reaches its rated<br>minimum value |
| 20                            | Enable/Disable Time      | T_oe                                     | -                | -    | 130        | ns                 |  |
| 21                            | Input Pull-up Impedence  | Z_in                                     | -                | 87   | 100        | K $\Omega$         | Pin 1, OE logic high or<br>logic low, or ST logic high           |
| 22                            | Drop Test                | 75 cm height,3 times on concrete floor . |                  |      |            |                    | JIS C6701  |



|    |                            |   |              |
|----|----------------------------|---|--------------|
| 23 | ESD Level                  | Human Body Model, class2: 2000V to 4000V; ANSI/ESDA/JEDEC JS-001-2010.  |              |
|    |                            | Machine Model, class B: 200V to 400V; ANSI/ESDA/JEDEC JS-001-2010.  |              |
| 24 | Moisture Sensitivity Level | Level 2.  |              |
| 25 | Mechanical Shock           | Device are shocked to half sine wave ( 1000 G ) three mutually. perpendicular axes each 3 times. 0.5m sec. duration time  | MIL-STD-202F |
| 26 | Vibration                  | Frequency range                    10 ~ 2000 Hz<br>Amplitude                                1.52 mm/20G<br>Sweep time                                20 minutes<br>perpendicular axes each test time    4 hours<br>(Total test time 12 hours) | MIL-STD-883E |

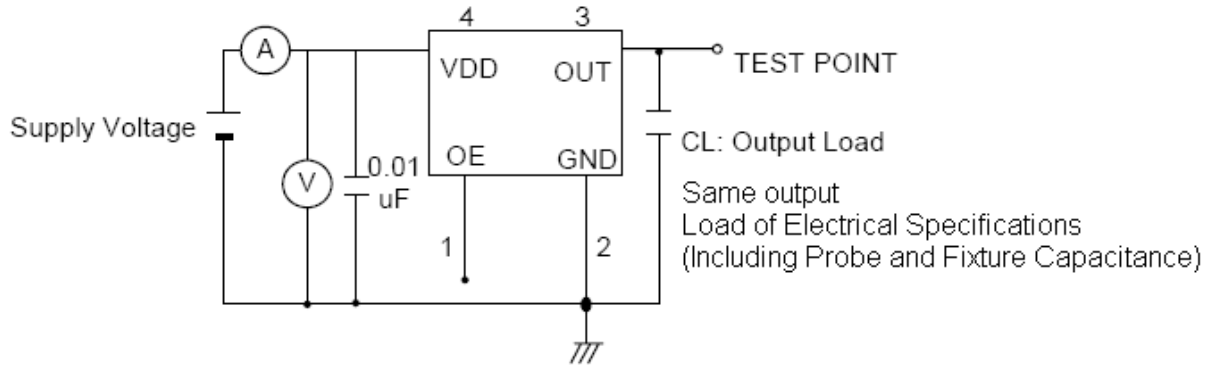
## 2、Mechanical Structure(mm)



**Note1:** Tolerance  $\pm 0.2\text{mm}$  without mark

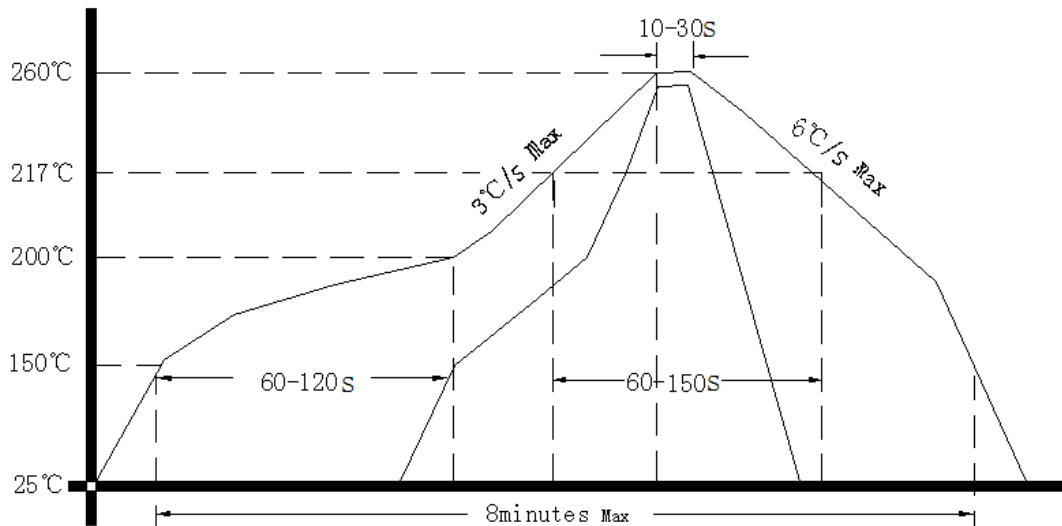


### 3、 Test Circuit



- Control input (output enable/disable)
- Logic 1 or open on pad 1: Oscillator output
- Logic 0 on pad 1 : Disable output to high impedance

### 4、 Reflow Soldering Curve (RoHS)



### 5、 Package: Tape & Reel (mm)

