



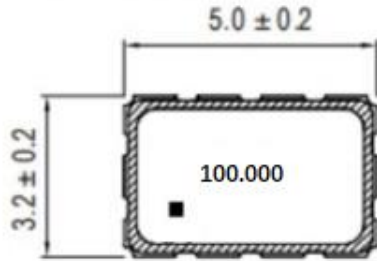
1、Electrical Parameters

MODEL: JDPCE1000003								
No.	Parameters	SYM.	Electrical Spec.				Notes	
			Min.	Typ.	Max.	Units		
1	Nominal Frequency	FL	100.00			MHz		
2	Output Waveform	-	LVDS					
3	Vdd	-	-0.5		4	V		
4	Supply Voltage	-	2.97	3.3	3.63	V		
5	Initial Frequency Tolerance	-	-25	~	+25	$\times 10^{-6}$	@25°C $\pm 2^{\circ}\text{C}$	
6	Frequency Tolerance vs. Operating Temperature Range	-	-50	~	+50	$\times 10^{-6}$		
7	Operating Temperature	T-opr	-55	~	+125	°C		
8	Storage Temperature	T-stg	-65	~	+150	°C		
9	Current Consumption	Idd	-	47	55	mA	Excluding load termination current, Vdd=3.3V	
10	Rise/Full Time	Tr、Tf		495	700	ps	20%~80%	
11	First Year Aging	-	-2		+2	$\times 10^{-6}$	@25°C	
12	10 Year Aging	-	-5		+5	$\times 10^{-6}$	@25°C	
13	Duty Cycle	DC	45		55	%		
14	Differential Output Voltage	VOD	250	350	450	mV		
15	Start up Time	T_start	-	6	10	ms	Measured from the time Vdd reaches its rated minimum value	
16	VOD Magnitude Change	ΔVOD			50	mV		
17	Offset Voltage	VOS	1.125	1.2	1.375	V		
18	VOS Magnitude Change	ΔVOS			50	mV		
19	Moisture Sensitivity Level	MSL1						

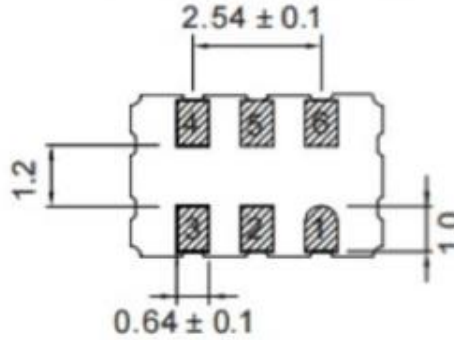


2、 Mechanical Structure(mm)

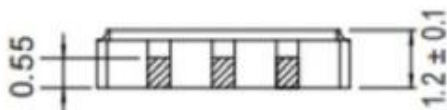
[TOP VIEW]



[BOTTOM VIEW]

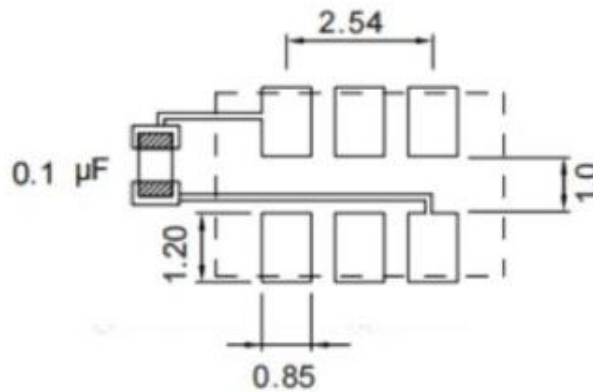


[SIDE VIEW]



Pin#	Function
1	OE
2	Nconnection
3	Ground
4	Output
5	Comp.Output
6	VDD

Solder PAD Layout



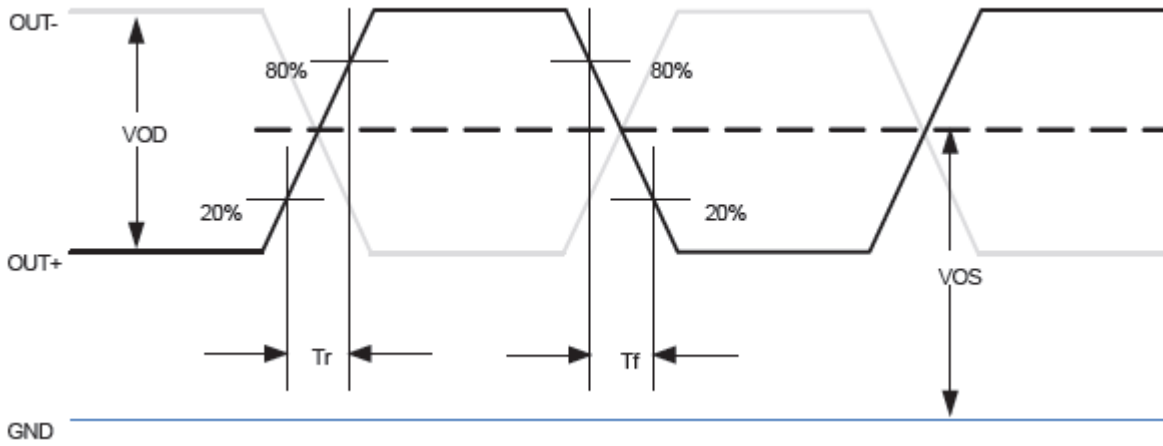
To maintain stable operation, provide a 0.01µF to 0.1µF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc-Ground)

Note1: Tolerance ± 0.2 mm without mark

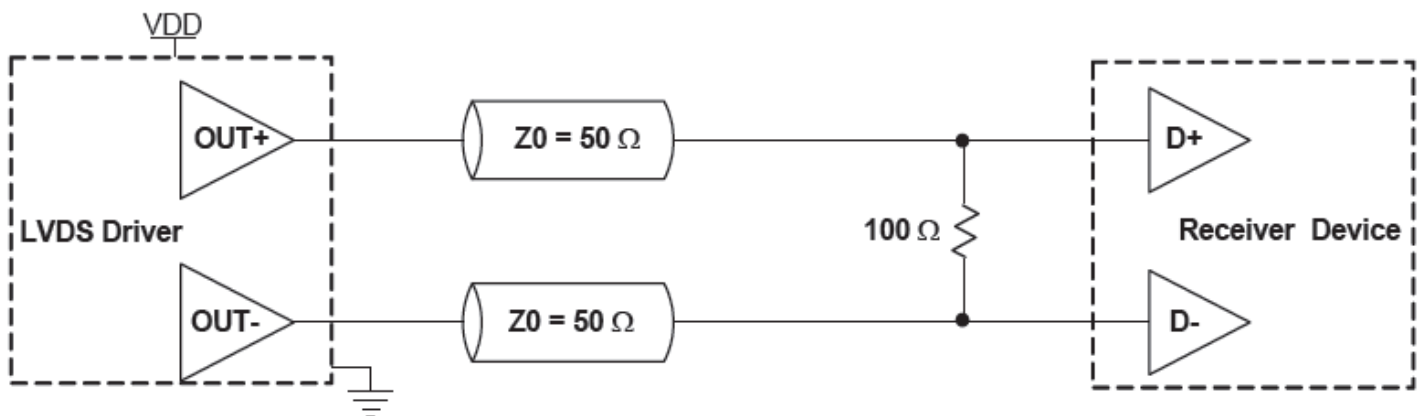
Note2: Referential weight 0.2g



3、Waveform Diagrams



4、Termination Diagrams



5、Package: Tape & Reel (mm)

