

Customer Code: \_\_\_\_\_

# DATASHEET

DAPU P/N: DP7W10000016

DAPU			Customer Approval
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2022.04.18			

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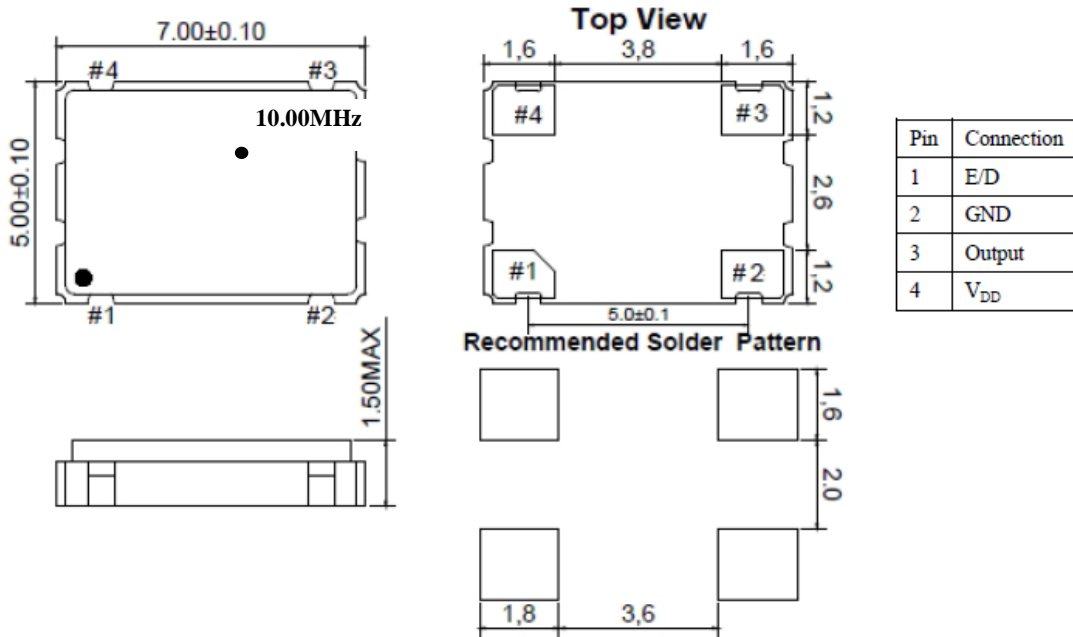


## 1、 Electrical Parameters

MODEL: DP7W10000016							
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL	10.00			MHz	
2	Output waveform	-	CMOS				
3	Output Load	CL	15			pF	
4	Frequency Stability	-	-50		+ 50	$\times 10^{-6}$	Include 25°C tolerance, operating temperature range , input voltage change,aging, load change.
5	Operating Temperature	Topr	-40	~	+ 85	°C	
6	Storage Temperature	Tstg	-55	~	+ 125	°C	
7	Supply Voltage	V <sub>DD</sub>	4.5	5.0	5.5	V	
8	Input Current	I <sub>cc</sub>			20	mA	
9	Output Voltage High	V <sub>OH</sub>	90%			V <sub>DD</sub>	
10	Output Voltage Low	V <sub>OL</sub>			10%	V <sub>DD</sub>	
11	Rise/Fall Time	T <sub>r</sub> 、 T <sub>f</sub>			6	ns	10%-90% V <sub>DD</sub> Level
12	Aging	-	-2		+ 2	$\times 10^{-6}$	First Year at 25°C
13	Pin 1, E/D function	pin 1=H or open.....output active at pin 3					
14		pin 1=L.....high impedance at pin 3					
15	Duty Cycle	-	40	~	60	%	
16	Start-Up Time	T <sub>start</sub>			10	ms	



## 2、Mechanical Structure(mm)



- Note:
- 0.01uF bypass capacitor should be placed between V<sub>DD</sub> (pin 4) and GND (pin 2) to minimize power supply line noise;
  - Line shouldn't be layed under the oscillator in the PCB to minimize signal interference.
  - The Load we advise is only 15pF (that means drive only 1 CMOS/TTL gate).

● MARKING

