

Customer Code: \_\_\_\_\_

# DATASHEET

DAPU P/N: DPZ7512M000033A0

DAPU			Customer Approval
Drew	Audited	Approved	Stamp, please! Thanks!
Jack	David	William	
Date: 2022.03.23			

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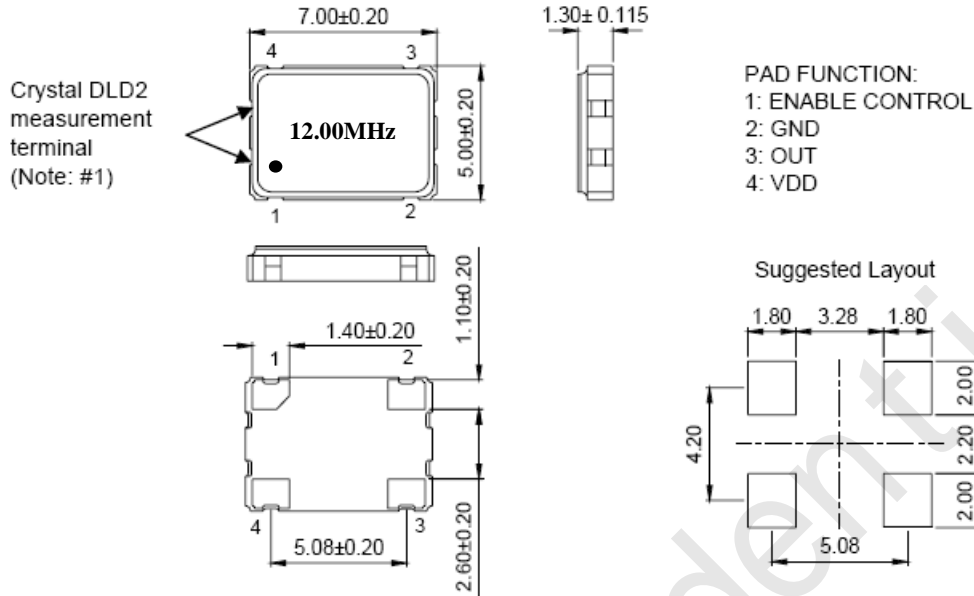


## 1、 Electrical Parameters

MODEL: DPZ7512M000033A0							
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL	12.00			MHz	
2	Frequency Tolerance	-	-20		+ 20	$\times 10^{-6}$	At 25°C
3	Frequency Stability(Overall)	-	-50		+ 50	$\times 10^{-6}$	Includes frequency tolerance@25°C and frequency stability VS.operating temperature range and voltage variance and load change and first year aging.
4	Operating Temperature	Topr	-40	~	+ 85	°C	
5	Storage Temperature	Tstg	-55	~	+ 125	°C	
6	Supply Voltage	V <sub>DD</sub>	2.97	3.3	3.63	V	
7	Input Current	I <sub>cc</sub>			15	mA	
8	Output waveform	-	CMOS				
9	Output Load	CL	15			pF	
10	Output Voltage High	V <sub>OH</sub>	90%			V <sub>DD</sub>	
11	Output Voltage Low	V <sub>OL</sub>			10%	V <sub>DD</sub>	
12	Rise/Fall Time	Tr、 Tf			4	ns	20%-80% V <sub>DD</sub> Level
13	Aging	-	-3		+ 3	$\times 10^{-6}$	First Year at 25°C
14	Tri-State Output Enable	-	70%			V <sub>DD</sub>	Pin 1, OE
15	Tri-State Output Disable	-			30%	V <sub>DD</sub>	Pin 1, OE
16	Duty Cycle	-	45	~	55	%	
17	Start-Up Time	Tstart			10	ms	Measured from the time V <sub>DD</sub> reaches its rated minimum value



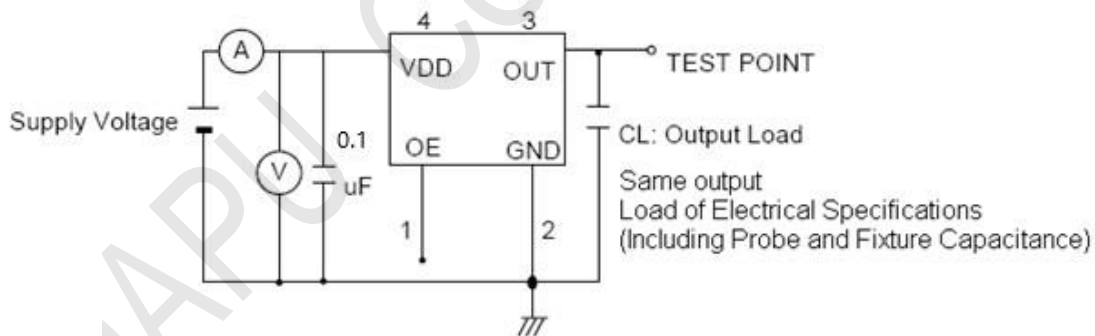
## 2、 Mechanical Structure(mm)



Note: #1. DLD2 / Drive Level Dependency 2 Maximum resistance minus minimum resistance.

Note1: Tolerance  $\pm 0.2$ mm without mark

## 3、 Test Circuit



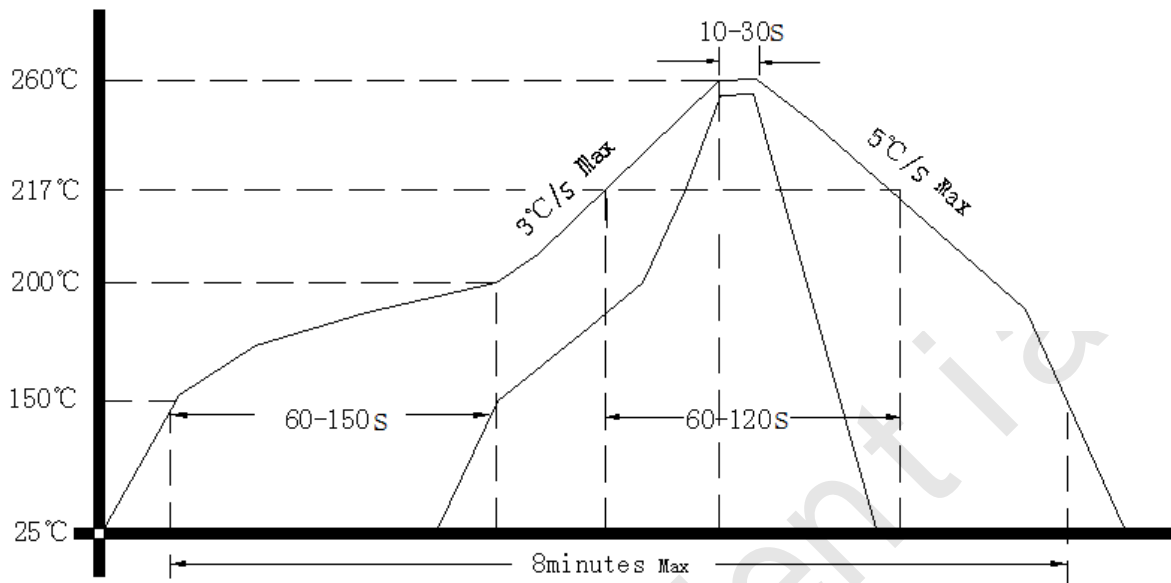
Control input (output enable/disable)

Logic 1 or open on pad 1: Oscillator output

Logic 0 on pad 1 : Disable output to high impedance



#### 4、 Reflow Soldering Curve (RoHS)



#### 5、 Package: Tape & Reel (mm)

