

Customer Code:

# DATASHEET

DAPU P/N: **CM55F-U125-10.00MHz**

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Plot			Customer Approval
Drew	Audited	Approved	Stamp, please! Thanks!
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Date: 2019.09.30			

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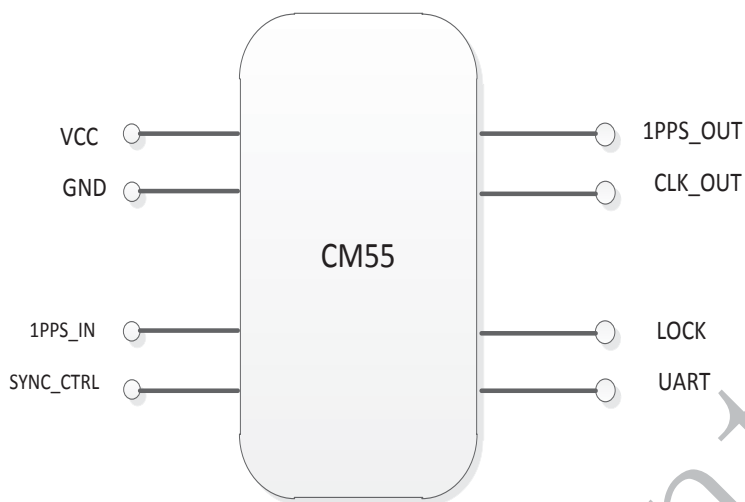


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## 1. General Description



**Figure 1 CM55**

Figure 1 is the basic diagram of CM55. CM55 is a high-performance clock module designed to provide precise frequency and phase synchronizing with external time reference for telecom and other applications.

### Key features:

- **Reference:** 1PPS and TOD from GNSS receiver, IEEE1588 etc.;
- **Temperature Stability:**  $\pm 0.2$ ppb;
- **Holdover:**  $\pm 10\mu\text{s}/24\text{h}$  @  $\Delta T = \pm 20^\circ\text{C}$  after power on/lock 12 hours;
- **Clocks Input and Output:** 1\*1PPS input, 1\*1PPS output and 1\*10MHz output;
- **Serial Interface:** 1\*UART for TOD and management;
- **Mechanical Size:** 51mm\*51mm\*18mm.



## 2. Pin Definition

**Table 1 Pin Definition**

Pin group	Pin#	Pin Name	Type	Description
Supply Voltage	4,11,13	GND	GND	Ground
	3	VCC	PWR	Power Supply
Control and Status Pins	8	SYNC_CTR L	I	Synchronizing Procedure Control
	5	LOCKED	O	Locked Status
UART	6	RXD0	I	Asynchronous Serial Data Output/Input
	7	TXD0	O	
Input Clock	10	1PPS_IN	I	1PPS Reference Input.
Output Clocks	12	1PPS_OUT	O	1PPS Output
	14	CLK_OUT	O	10.00MHz Output
Reserve	1, 2, 9	NC		Not connected.

## 3. Electrical Parameters

**Table 2 Electrical Parameters**

Parameter	Symbol	Minimum	Typical	Maximum	Units
<b>LVC MOS Input</b>					
High Level Input Voltage	$V_{IH}$	2.4			V
Low Level Input Voltage	$V_{IL}$			0.4	V
<b>LVC MOS Output</b>					
High Level Output Voltage	$V_{OH}$	2.4			V
Low Level Output Voltage	$V_{OL}$			0.4	V



## 4. Performance

**Table 3 Performance**

Item	Parameter	Minimum	Typical	Maximum	Units	Test Condition	
Clock Output	Nominal Frequency	10.00			MHz	Synchronizing with 1PPS reference.	
	Duty Cycle	45	50	55	%	Load 15pF	
	Frequency vs. Temperature	-0.2		+0.2	$\times 10^{-9}$	V <sub>cc</sub> =5.0V; O <sub>load</sub> =15pF;T <sub>A</sub> varies from -40°C to 70°C, temperature slope less than 2°C per minute.	
	Accuracy	-1		+1	$\times 10^{-12}$	24 hours average value when locked to 1PPS.	
	Short-term Stability	-0.02		+0.02	$\times 10^{-9}$	V <sub>cc</sub> =5.0V;T <sub>A</sub> =25°C; 1s; using PN9000; no EMI\EMC or other interference.	
	Daily Aging	-0.5		+0.5	$\times 10^{-9}$	V <sub>cc</sub> =5.0V;T <sub>A</sub> =25°C.	
	Yearly Aging	-0.03		+0.03	$\times 10^{-6}$		
	Phase Noise			-115		dBc/Hz	10Hz
				-135			100Hz
				-145			1KHz
			-150		10KHz		
			-150		100KHz		
			-150		1MHz		
1 PPS Output	Pulse Width		100		ms		
	Accuracy	-30		+30	ns	Synchronizing with 1PPS reference. Mean is 0, and standard deviation is 20ns. After locked, monitor 24 hours.	
	24 hours holdover	-10		+10	μs	ΔT= ±20°C,24 hour holdover after locked12 hours. Temperature slope less than 3°C per minute.	
Supply	Supply	4.75	5.0	5.25	V		



Voltage	Voltage					
	Warm Up Current			700	mA	
	Steady Current			280	mA	@25°C
	AC Ripple			50	mVpk-pk	10Hz to 1MHz

### 5. UART

UART inter face sare used for management and TOD, which has a fixed baud rate (115200) using 1 stop bit and no parity. It is a LVTTTL-compatible port and needs an external translator to work with other signal types (such as RS-232C or RS-485).

### 6. Control and Status Pins

CM55 is a clock module which synchronizes the local clock to reference such as 1 PPS retrieving from GPS. CM30 will work normally performing synchronizing algorithm when the SYNC\_CTRL pin is driven high. It also could be force to work in free-run or holdover status when the SYNC\_CTRL pin is driven low.

The LOCKED pin indicates the lock status of CM55. High level indicates the module is locked to external 1PPS reference. When the module never is locked to reference after power up, the status of module is free-run, the LOCKED pin outputs low. When the reference is lost, the status of module is holdover, the LOCKED pin also output low.



## 7. Environmental Conditions

**Table 4 Environmental Conditions**

Parameter	Conditions	
Operating Temperature	-40°C to 70°C	
Storage Temperature	-55°C to 105°C	
Storage Humidity	30%~80%	
ESD Level	Human Body Model, class2: 2000V to 4000V; ANSI/ESDA/JEDEC JS-001-2010.	
	Machine Model, class B: 200V to 400V; ANSI/ESDA/JEDEC JS-001-2010.	
Moisture Sensitivity Level	Not humidity sensitive.	
Vibration	Test Condition: 0.75mm ;acceleration:10g;10Hz~500Hz, one cycle per 30 min, test 2 hour. (3 times for each 3 directions X ,Y , Z), IEC 68-2-06 Test Fc.	
Shock	50g; 11ms; half sine wave (3 times for each 3 directions X,Y,Z),IEC 68-2-27 Test Ea/Severity 50A.	
Relative Humidity	20%~70%	Full Package Storage
Temperature	-10°C~35°C	





## 8. Typical Application

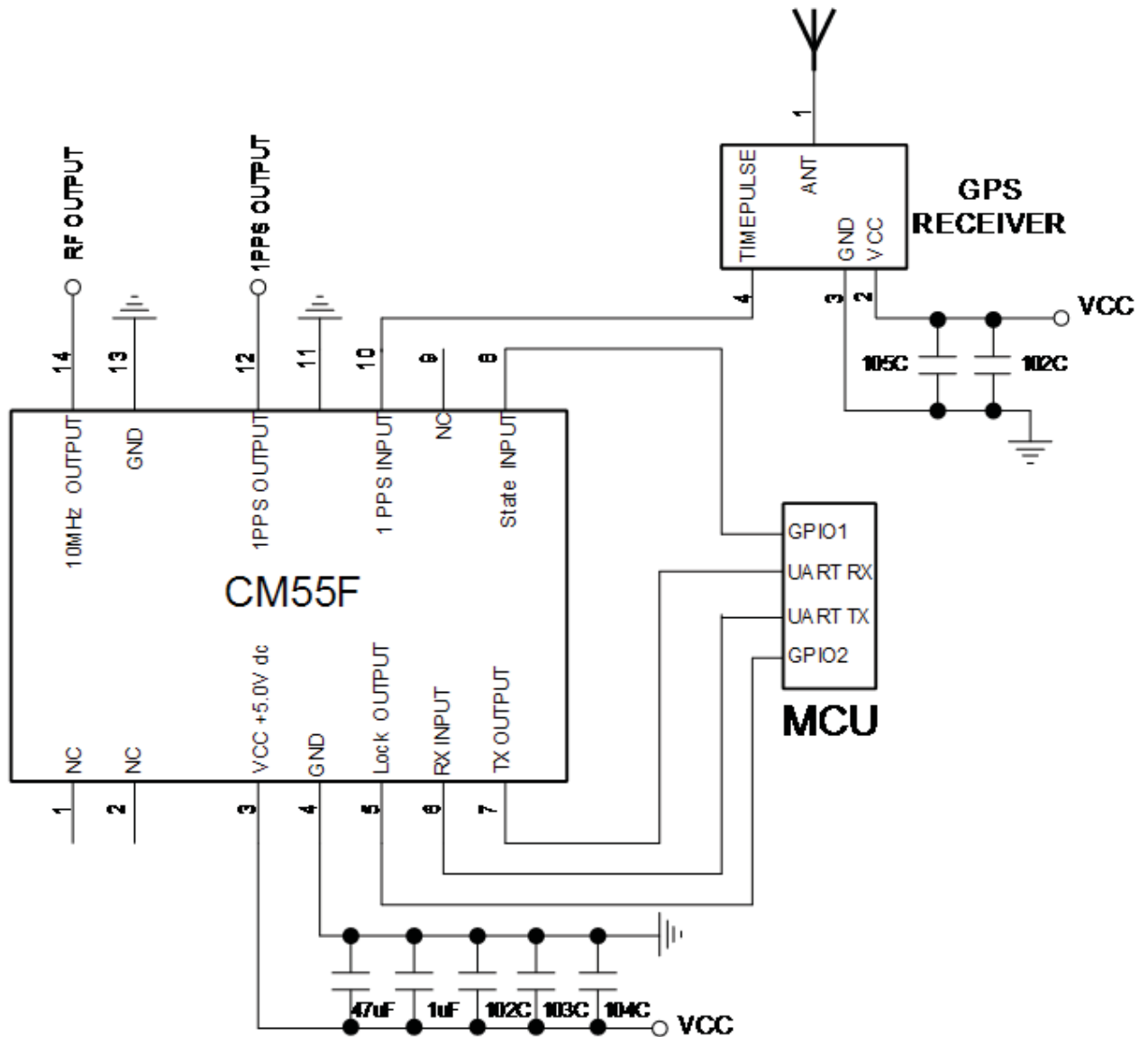


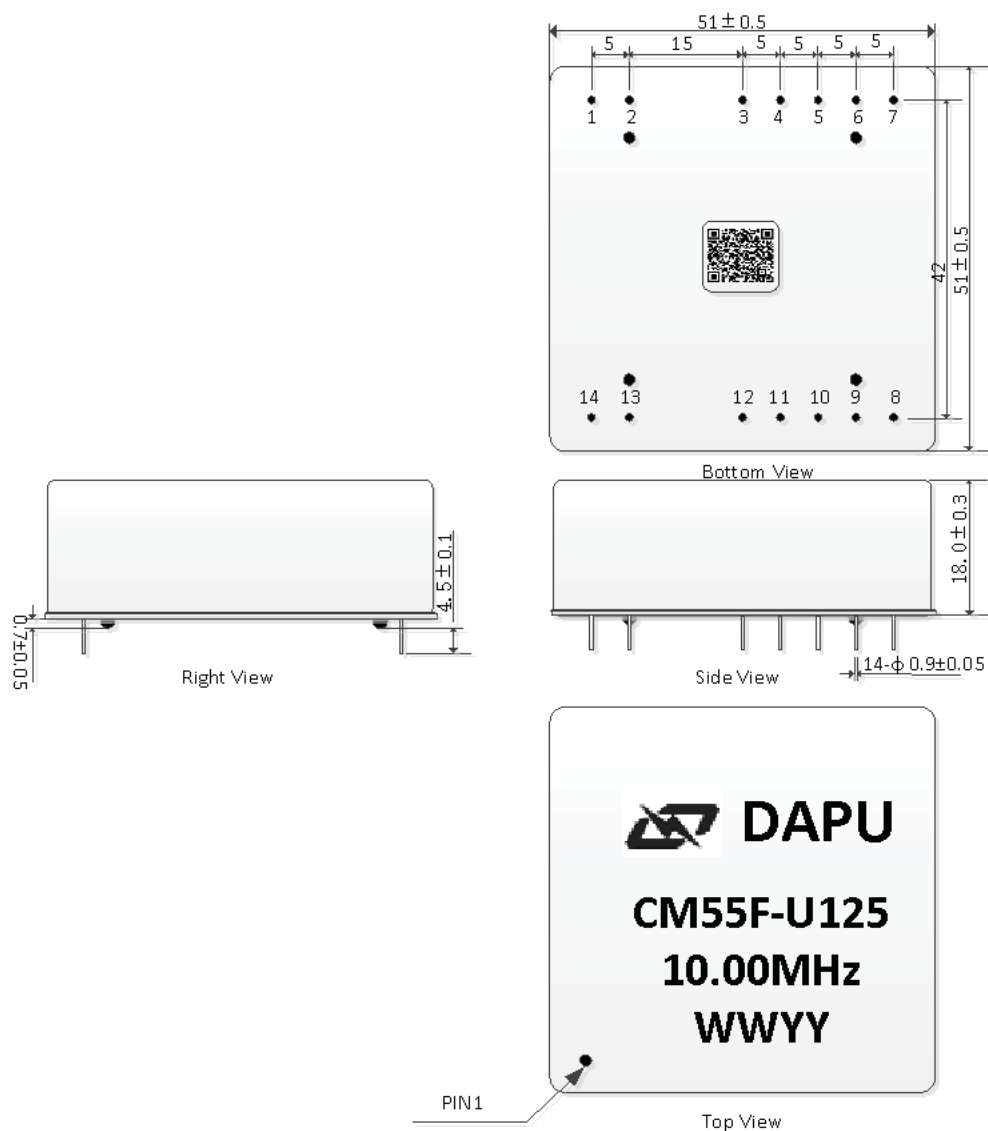
Figure 2 Typical application

GNSS Receiver offers 1PPS signal to CM55.

The MCU monitors the work state of CM55.



### 9. Mechanical Structure (mm)



**Note1:** Tolerance  $\pm 0.3$ mm without mark.

**Note2:** WW represents Week.

YY represents Year.



### 10. Wave Soldering Curve(RoHS)

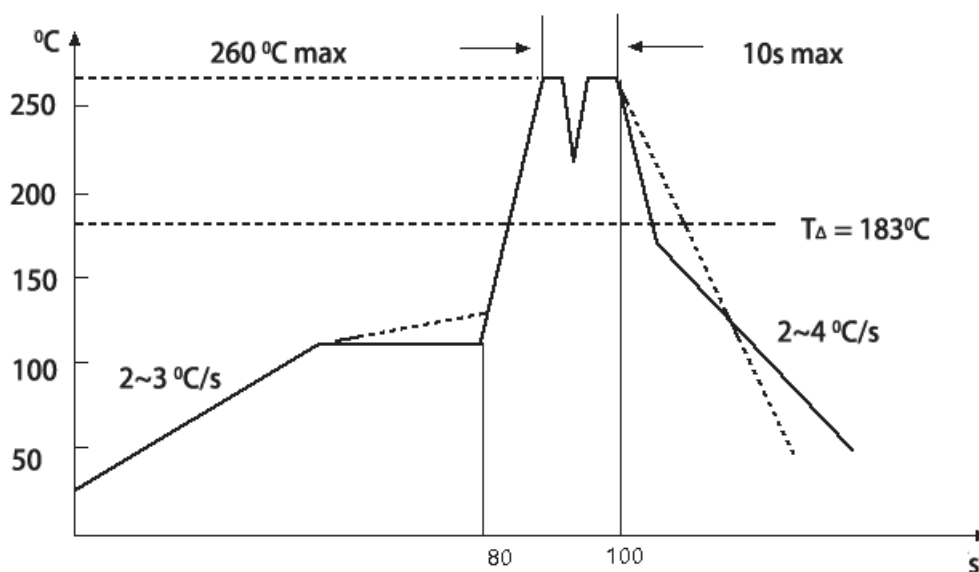


Figure 4 Wave soldering curve

### 11. Package (mm)

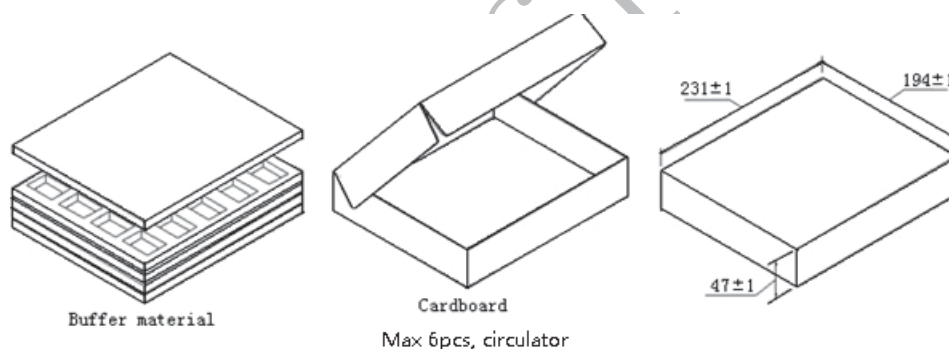


Figure 5 Package