

Customer Code : \_\_\_\_\_

# DATASHEET

DAPU P/N: DP9W32768001

Plot			The Label
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2018.05.25			

## Guangdong Dapu Telecom Technology Co.,Ltd

Bldg16,.N.Ind.Zone,SSL Industry Park, Dongguan City, Guangdong Province, China

TEL: 0086-0769-88010888 FAX: 0086-0769-81800098



## 1、Electrical Parameters

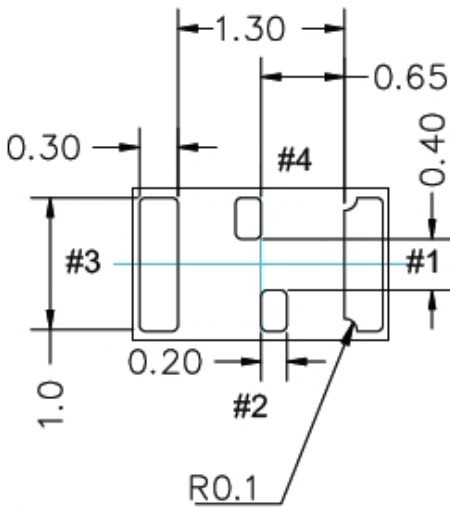
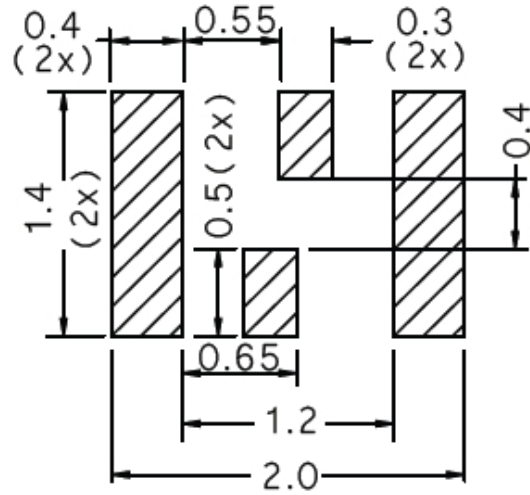
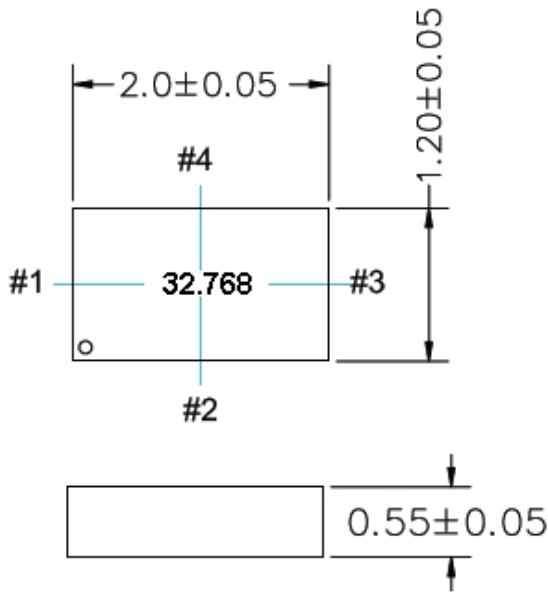
MODEL: DP9W32768001							
No.	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL	32.768			KHz	
2	Output Waveform		LVCMOS				
3	Frequency Tolerance <sup>2</sup>	F-tol			20	$\times 10^{-6}$	TA=25 °C,postreflow,Vdd:3.13V~3.47V
4	Operating Supply Voltage	Vdd	3.13	3.3	3.47	V	TA=-40~85 °C
5	Frequency Stability <sup>3</sup>	F-stab			100	$\times 10^{-6}$	TA=-40~85 °C, Vdd:3.13V~3.47V
6	Operating Temperature	T-opr	-40	~	+85	°C	
7	Power-supply Ramp		-		100	ms	TA=-40~85,0 to 90% vdd
8	Output Stage Operating Current <sup>4</sup>	Idd-out		0.065	0.125	$\mu\text{A/vpp}$	TA=-40~85 °C,Vdd:3.13V~3.47V, no load
9	Core Operating Current <sup>4</sup>	I_DD			1.4	$\mu\text{A}$	TA=-40~85 °C, Vdd max:3.47V,no load
10	Output Rise/Full Time	Tr、Tf		100	200	ns	10~90%(Vdd),15pF load, Vdd:3.13V~3.47V
11	Output Clock Duty Cycle	DC	48		52	%	
12	Output Voltage High	VOH	90%	-	-	Vdd	Vdd:3.13V~3.47V,Ioh=-10 $\mu\text{A}$ ,15pF
13	Output Voltage Low	VOL	-	-	10%	Vdd	Vdd:3.13V~3.47V,Iol=10Ma,15Pf
14	Aging		-1	-	+1	$\times 10^{-6}$	1 <sup>st</sup> year @25 °C
15	Start up Time Power-up <sup>5</sup>	T_start	-	180	300	ms	TA=-40 °C $\leq$ T <sub>A</sub> $\leq$ 50 °C, valid output
					450		TA=50 °C $\leq$ T <sub>A</sub> $\leq$ 85 °C, valid output
16	Storage Temperature		-65		105	°C	

**Notes:**

- Measured peak-to-peak, Tested with Agilent 53132A Frequency counter, Due to the low Operating Temperature, the gate time must be  $\geq 100\text{ms}$  to ensure an accurate frequency measurement.
- Stability is specified for two operating voltage ranges, Stability progressively degrades with supply voltage below 3.13V, Measure peak-to-peak, Inclusive of initial Tolerance at 25 °C, and variations over operating temperature, rated power supply voltage and load.
- Core operating current does not include output driver operating current or load current, To derive total operating current (no load), add core operating current  $+(0.065\mu\text{A/V}) * (\text{peak-to-peak output Voltage swing})$
- Measured from the time Vdd reaches 3.13V.



## 2、Mechanical Structure(mm)



### Pin Configuration

SMD Pin	Symbol	I/O
1	NC	No Connect
2	GND	Power Supply Ground
3	CLK Out	OUT
4	Vdd	Power Supply

unit:mm

Note1:Tolerance ±0.2mm without mark



### 3、 System Block Diagram

