

Customer Code : \_\_\_\_\_

# DATASHEET

DAPU P/N: **DP7X4000009**

Plot			The Label
Drew	Audited	Approved	
			Stamp, please! Thanks!
Date: 2017.07.11			

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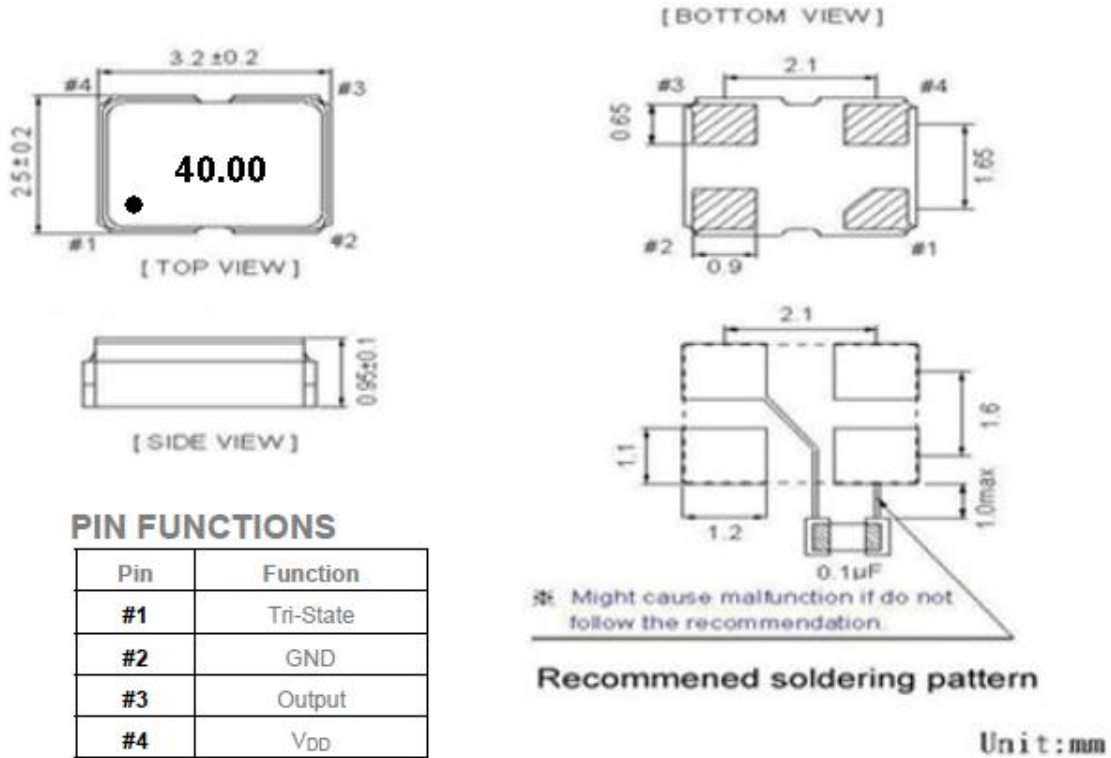


## 1、Electrical Parameters

MODEL: DP7X4000009							
No	Parameters	SYM.	Electrical Spec.				Notes
			Min.	Typ.	Max.	Units	
1	Nominal Frequency	FL	40.00			MHz	
2	Output Waveform		CMOS				
3	Supply Voltage		1.65		3.45	V	
4	Frequency Stability	F-stab	-20		+20	$\times 10^{-6}$	@25°C
5	Operating Temperature	T-opr	-40	~	+85	°C	The operating temperature range over which the frequency stability is measured.
6	Storage Temperature	T-stg	-55	~	+125	°C	
7	Current	Icc	-		5.0	mA	At maximum supply voltage
8	Rise/Full Time	Tr、 Tf			5	ns	
9	Output Load				15	pF	
10	Aging		-3		+3	$\times 10^{-6}$	Frequency drift in first year
11	Duty Cycle	DC	45	50	55	%	
12	Output Voltage High	VOH	90%	-	-	Vdd	
13	Output Voltage Low	VOL	-	-	10%	Vdd	
14	Tri-State	Output Active	0.7 Vdd	-	-	V	PIN 1 Tri-State
		Output in High-Impedance state	-	-	0.3 Vdd	V	
15	Start up Time	T_start	-	-	5	ms	
16	Vibration Test	MIL-STD-883 2007 Condition A	10~2000Hz, 1.52mm, 20g, each axis for 4 hrs				
		JESD22-B103 Condition 1					
	Thermal Shock	MIL-STD-883 1010 Condition B	-55°C, 125°C; soak time is 10 mins, with total 200 cycles				
		JESD22-A104 Condition B					
	Mechanical Shock	MIL-STD-883 2002 Condition B	1500g, half-sine, 0.5ms, each axis for 3 times.				
JESD22-B104 Condition B							

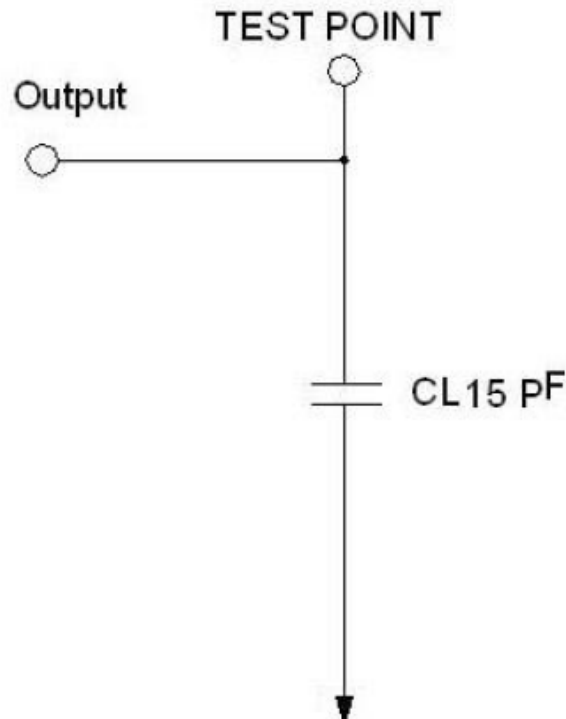


## 2、 Mechanical Structure(mm)



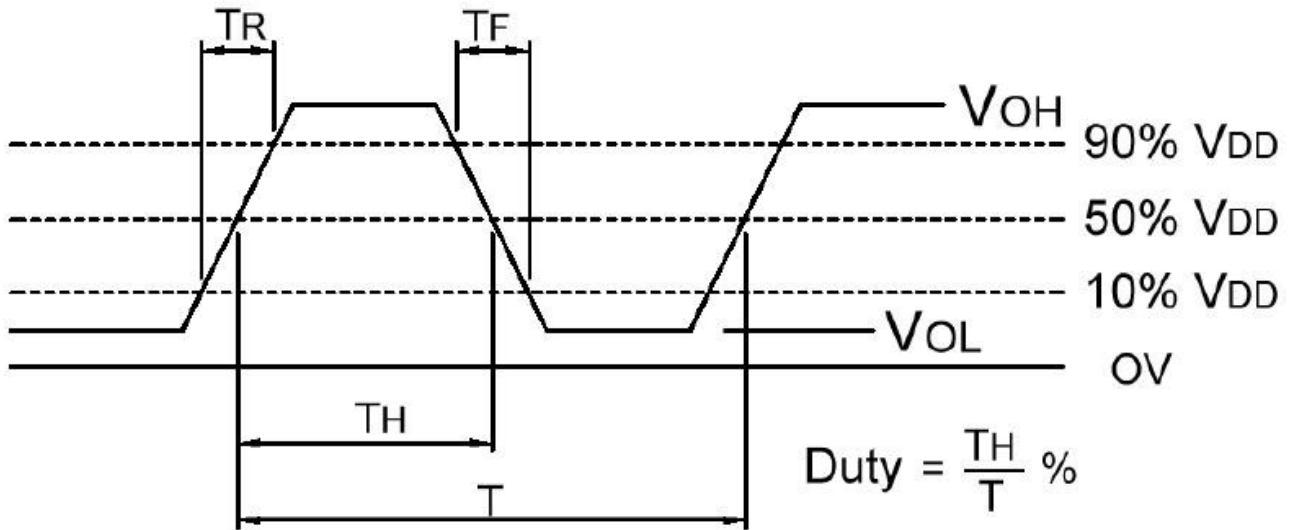
**Note1:**Tolerance  $\pm 0.2$ mm without mark

## 3、 Test Circuit (CMOS LOAD)



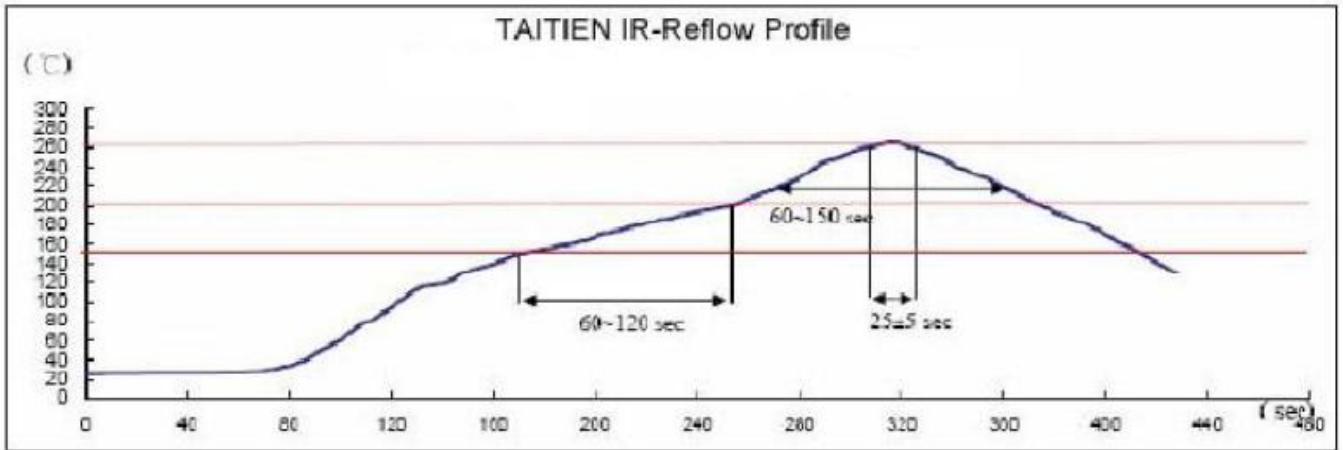


#### 4、 Output Waveform(CMOS LOAD)



#### 5、 RECOMMENDED IR REFLOW PROFILE

##### ➤ IR REFLOW PROFILE OF CERAMIC SMD PRODUCTS FOR Pb FREE PROCESS



##### IR-Reflow Test

Reference Standard : JEDEC-STD 020

Test Conditions: Pre-heating : 150°C to 200 °C, 60~120 sec

Heating : 217 °C , 60~150 sec

Peak Temperature : 260±5 °C, 25±5 sec



## 6、 Package: Tape & Reel (mm)

