

Customer Code : _____

DATASHEET

DAPU P/N: CM33P-S329-10.00MHz

Customer P/N: _____

DAPU			Customer Approval
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2015.12.15			

Guangdong Dapu Telecom Technology Co.,Ltd

Bldg 16,.N.Ind.Zone,SSL Industry Park, Dongguan City, Guangdong Province, China

TEL: 0086-0769-88010888 FAX: 0086-0769-81800098



1. Electrical Parameters

	Parameters		Min.	Typ.	Max.	Unit.	Test Condition
	1 PPS Reference Input	Waveform		HCMOS			
High-Level Output Voltage (V_{IH})		2.4			V	50 Ω	
Low-Level Output Voltage (V_{IL})				0.4	V		
Pulse Width		10			μ s		
Connector		Pin 4					
SGMII Interface	Parameters		Min.	Typ.	Max.	Unit.	Test Condition
	V_i	Input Voltage Range	675		1725	mV	
	Width	Input Differential Threshold	50		400	mV	
	V_{od}	Input Differential Voltage	150		400	mV	
	R_{in}	Differential Input Impedance	80		120	Ω	
	DRT	Serial Input Data Rate Tolerance	-300		+300	ppm	
	V_{oh}	Output Voltage High State			1525	mV	
	V_{ol}	Output Voltage Low State	875			mV	
	V_{od}	Output Differential Voltage	150		400	mV	
	DR	Serial Data Rate		1.25		Gbits/sec	
	Connector		Pin 26,27,28,29 Pin 35,36,37,38				
RST	Parameters		Min.	Typ.	Max.	Unit.	Test Condition
	Normal State		2.4			V	<5mA Load
	Reset to Default				0.4	V	<5mA Load
	Minimum Time		100			ns	
	Connector		Pin 24				
Holdover Capability	Holdover Time		Min.	Typ.	Max.	Unit.	Test Condition
	24 Hours		-44		44	μ s	$\Delta T = \pm 5^\circ C$, 24 hours holdover after turn on and lock 3 days. Temperature variable speed less than 1 $^\circ C$ per minute



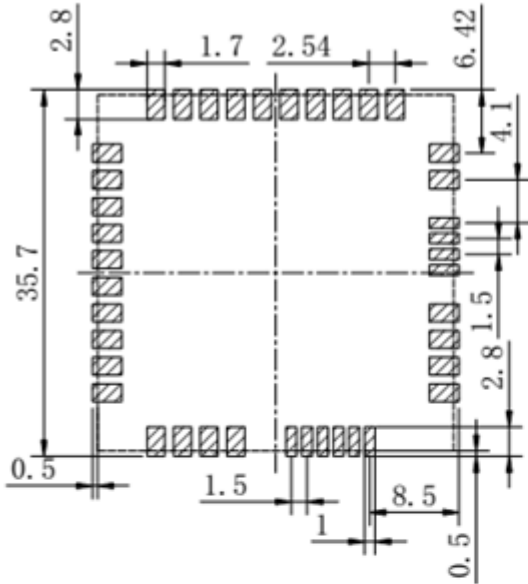
	Parameters	Min.	Typ.	Max.	Unit.	Test Condition
Supply Voltage	Supply Voltage	3.13	3.3	3.47	V	
	Current Consumption			1100	mA	During Warm-up
				700	mA	During steady state operation @25°C
	AC Ripple			50	mV pk-pk	10Hz to 1MHz
	Connector	Pin11				
	Parameters	Min.	Typ.	Max.	Unit.	Test Condition
1 PPS Output Waveform Characteristics	Waveform	HCMOS				
	High-Level Output Voltage(V_{OH})	2.4			V	50Ω
	Low-level Output Voltage (V_{OL})			0.4	V	
	Pulse Width	0.1	100	400	ms	
	Phase Accuracy	-50		50	ns	CM is master mode, sync with the GPS 1PPS
	Connector	Pin 16				
	Parameters	Min.	Typ.	Max.	Unit.	Test Condition
LOCK Status Output	Lock	2.4			V	<5mA Load
	Free run/Holdover			0.4	V	<5mA Load
	Connector	Pin 32				
	Parameters	Min.	Typ.	Max.	Unit.	Test Condition
TOD	Rx High-level Input Voltage (VH)	2.4			V	
	Rx Low-level Input Voltage (VL)			0.4	V	
	Tx High-level Output Voltage (VH)	2.4			V	
	Tx Low-level Output Voltage (VL)			0.4	V	
	Serial Protocol	4800-N-8-1				
	Connector	Pin 17,18 Pin 19,20				



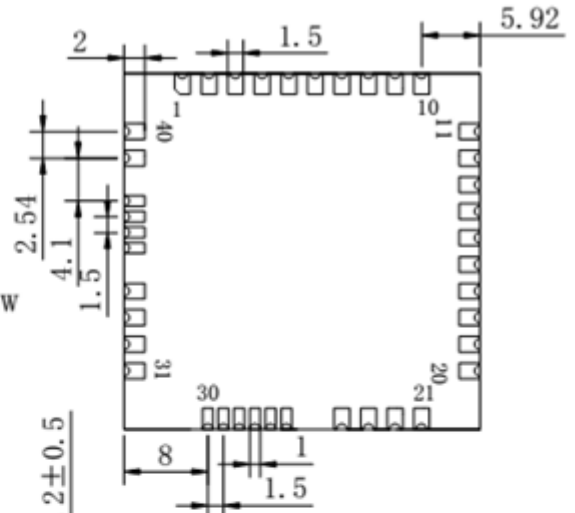
Parameters	Min.	Typ.	Max.	Unit.	Test Condition
	FREQ_OUT0/ FREQ_OUT1	Time-aligned: programmable frequency 100Hz to 25MHz Frequency-aligned: programmable frequency 1 kHz to 62.5MHz			
Waveform	HCMOS				
High-Level Output Voltage(V _{OH})	2.4			V	15pF
Low-level Output Voltage (V _{OL})			0.4	V	
Rise/Fall Time			8	ns	
Connector	Pin 14,34				
Environmental Conditions	Parameter	Conditions			
	Operating Temperature	-20°C to +75°C			
	Storage Temperature	-55°C to +105°C			
	Storage Humidity	30%~80%			
	ESD Level	Human Body Model,class2: 2000V to 4000V; ANSI/ESDA/JEDEC JS-001-2010.			
		Machine Model, class B: 200V to 400V; ANSI/ESDA/JEDEC JS-001-2010.			
	Moisture Sensitivity Level	Not humidity sensitive.			
	Vibration	Test Condition: 0.75mm ;acceleration:10g;10Hz~500Hz, one cycle per 30 min, test 2 hour. (3 times for each 3 directions X ,Y , Z), IEC 68-2-06 Test Fc.			
Shock	50g; 11ms; half sine wave (3 times for each 3 directions X ,Y , Z),IEC 68-2-27 Test Ea/Severity 50A.				
Full Package Storage	Relative Humidity (%)	20%~70%			
	Temperature (°C)	-10~35°C			



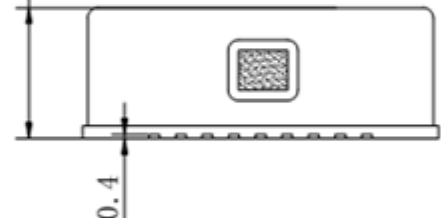
2. Mechanical Structure(mm)



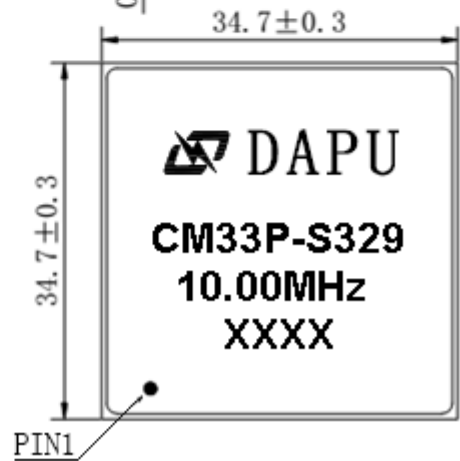
Bottom view



Side view



Top view



Note1: Tolerance $\pm 0.20\text{mm}$ without mark

Note2: The first two xx representative: week
After two xx representative: year

**Pin Description**

PIN	NAME	I/O	DESCRIPTION
1	OSC_CLK_OUTPUT	O	Suspended, reserved
2	CLK_SEL	I	System clock select: 1: the system clock use the local oscillator 0: the system clock the external reference
3	FORCE_HOLD	I	Reserved, update by software
4	1PPS_IN	I	1PPS input clock, it can come from GPS receiver or other 1PPS reference
5,15,25,30	GND		GND
6	SLVINT	O	SPI interface, The serial peripheral interface (SPI) is a slave port for communication with a serial microprocessor bus, allowing the module to be controlled by an external processor
7	SLVCSB	I	
8	SLVSCLK	I	
9	SLVMISO	O	
10	SLVMOSI	I	
11	VCC		VCC
12,13	NC		Suspended, reserved
14	FREQ_OUT0	O	Time-aligned output pair: 125 MHz divided by n (n = 4 to 125000) maximum of 25MHz MHz (divide by 4) minimum of 100 Hz (divide by 1249999)
34	FREQ_OUT1	O	Frequency-aligned outputs: programmable frequency 1 kHz to 62.5MHz
16	1PPS_OUT	O	The clock module 1PPS output
17	TOD1_TX	O	Time of day output interface, NMEA 0183, The UART has an integrated baud rate generator using 1 stop bit and no parity
18	TOD1_RX	I	
19	TOD0_RX	I	
20	TOD0_TX	O	
21	MDC	O	MII CLK
22	MDIO	I/O	MII data input/output
23	OSCFSEL0	I	Oscillator frequency select pins, which set the expected local oscillator frequency
33	OSCFSEL1	I	OSCFSEL1/0=00 local oscillator frequency 20MHz OSCFSEL1/0=01 local oscillator frequency 10MHz OSCFSEL1/0=10 local oscillator frequency 12.8MHz
24	RST	I	Reset the clock module
26	SGMIITXP0	O	PTP port 0,SGMII interface
27	SGMIITXN0	O	
28	SGMIIRXP0	I	
29	SGMIIRXN0	I	
31	FAULT	O	Fault alarm

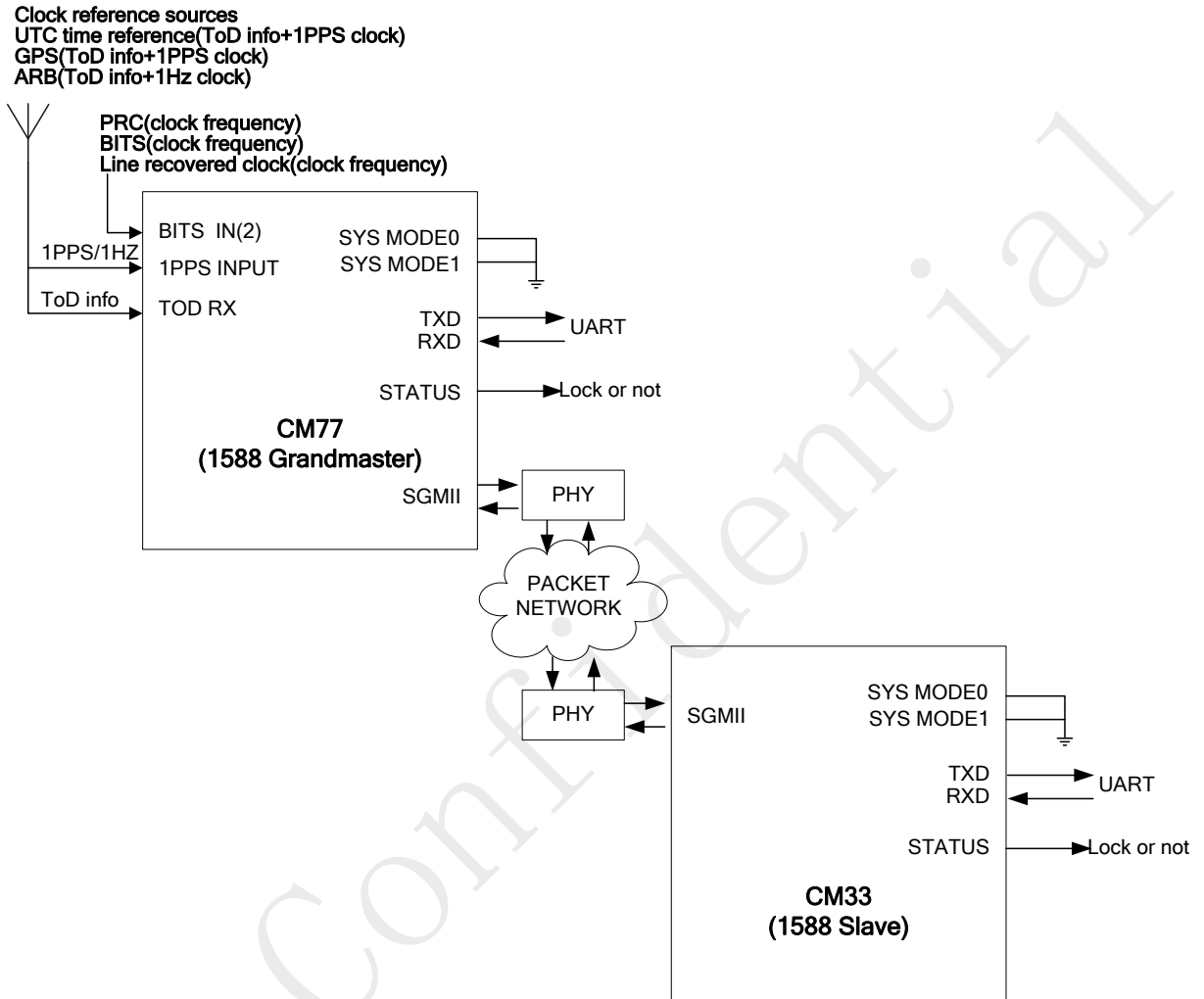


32	LOCKED	O	State output. Output high level when the CM is locked and stable, others low level
35	SGMIIRXN1	I	PTP port 1,SGMII interface
36	SGMIIRXP1	I	
37	SGMIITXN1	O	
38	SGMIITXP1	O	
39	FREQ_IN	I	Clock reference input. Acceptable frequencies into the PTP module from: 1 PPS/1 Hz to 161MHz (input reference for PTP Master) The input frequencies must obey the following rule: Input Freq = $k * 2^n$, where $0 \leq n \leq 5$ and $1 \leq k \leq 2^{32}$ (upper limit of 170 MHz)
40	EXT_CLK	I	External clock , back up for the local oscillator (support frequency 10MHz,12.8MHz,20MHz)



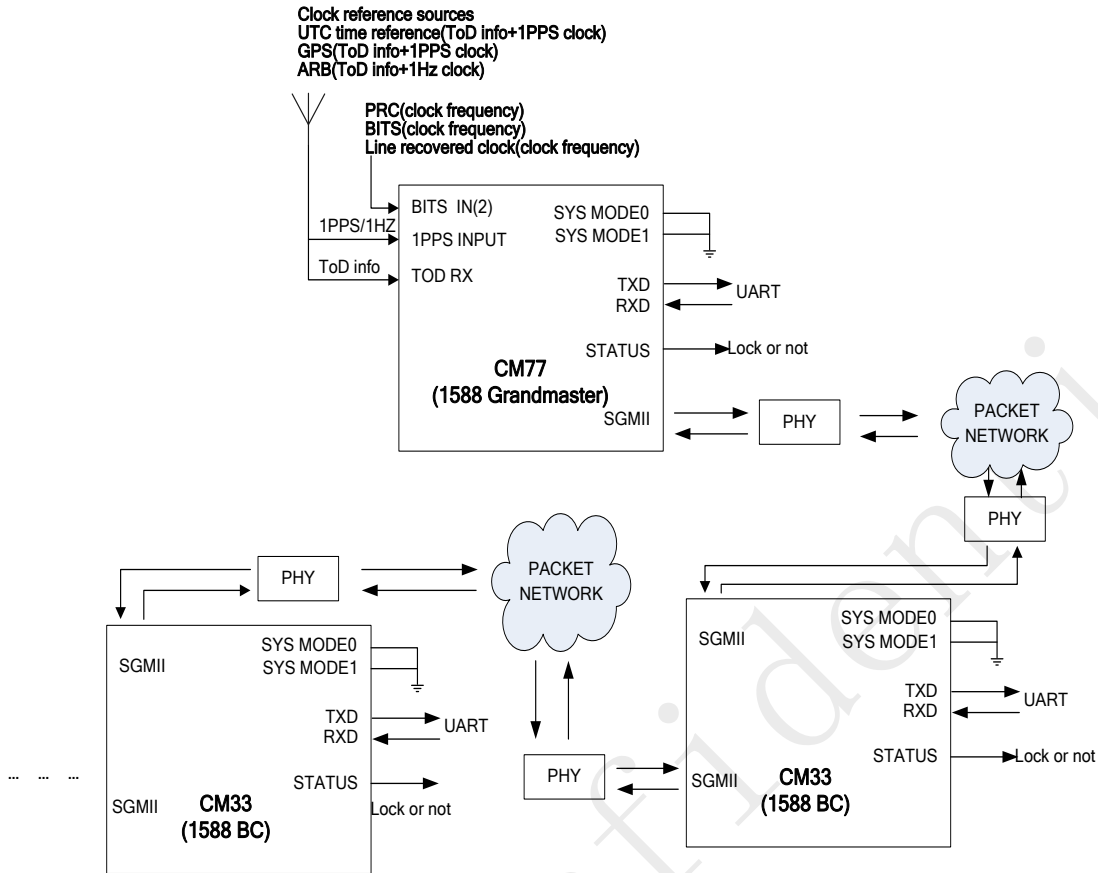
3. Application Information

Typical application 1



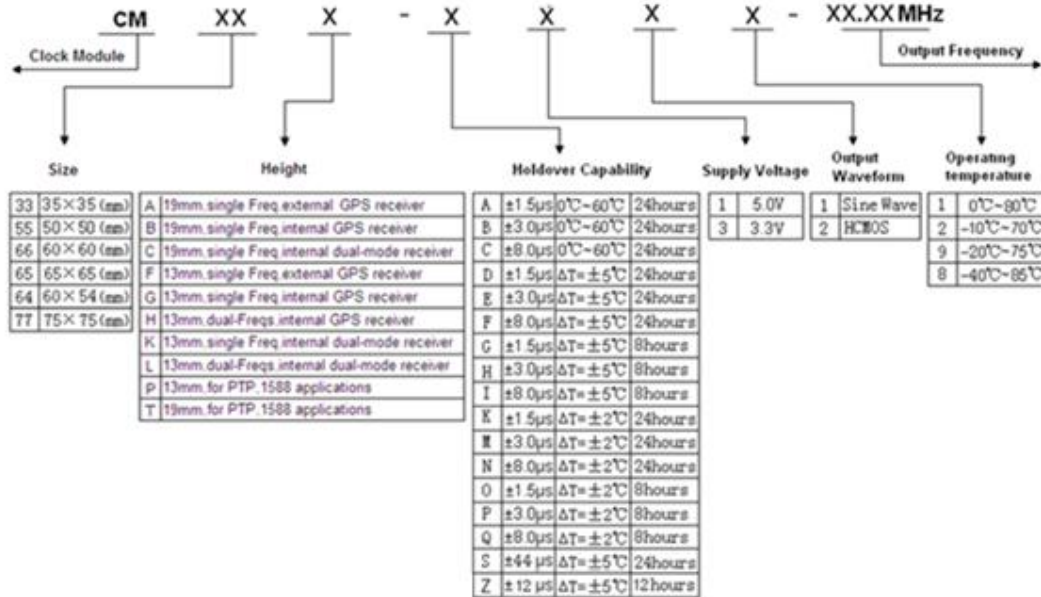


Typical application 2

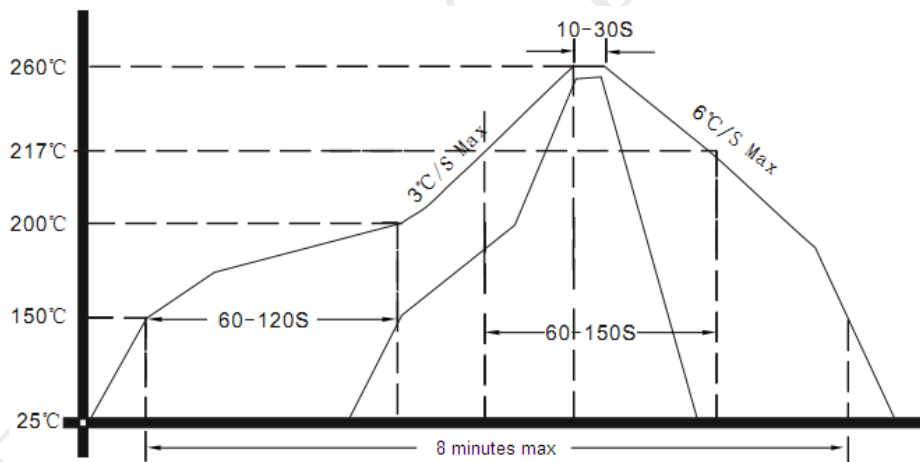




4. Coding Rules



5. Reflow Soldering Curve (RoHS)



6. Package (mm)

