

Travelling Merchant: \_\_\_\_\_

# DATASHEET

Standard: VC936B-CEAD-76.80MHz

Plot			The Label
Drawing	Auditing	Approve	Stamp, please! Thanks!
Date:			

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## The parameter of product

### MODEL: VC936B-CEAD-76.80MHz

#### 1. Output

1.1. Frequency	76.80MHz
1.2. Waveform	LVPECL
1.3. Symmetry	45%~55% @1,65 V
1.4. Rise / Fall time (20%~80%)	1ns max.
1.5. Load	50 $\Omega$
1.6.Jitter	$\leq 1$ ps RMS (12KHz ~20MHz)
1.7. Harmonics	<-12dBc

#### 2. Frequency Stability

2.1. Tolerance vs. Temperature Rang	$\leq \pm 3.0 \times 10^{-5}$	@-40°C ~ +85°C ,ref. to 25 °C
2.2. Tolerance vs. Supply Voltage	$\leq \pm 5.0 \times 10^{-6}$	@3.3VDC $\pm 5\%$
2.3. Stability vs. Load change	$\leq \pm 1.0 \times 10^{-6}$	@Load $\pm 10\%$
2.4. Aging	$\leq \pm 5.0 \times 10^{-6}$ / frist year	

#### 3. Voltage

3.1. Supply Voltage	+3.3VDC $\pm 5\%$
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#### 4. Current

4.1. Supply current	$\leq 65$ mA (Typical)
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#### 5. Electrical frequency adjustment

5.1. Control Voltage = 0.2 V	-45ppm~-110ppm
5.2. Control Voltage = 1.65 V	-10ppm~+10ppm
5.3. Control Voltage = 3.1 V	+45ppm~+110ppm
5.4. Linearity	$\leq \pm 20\%$
5.5. Input impedance	$\geq 1$ M $\Omega$
5.6. Slope	Positive

#### 6. Phase noise(Typical)

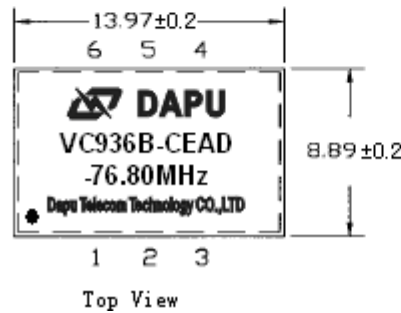
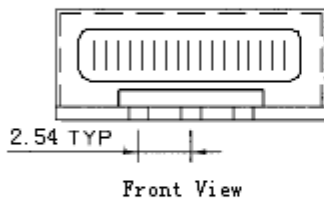
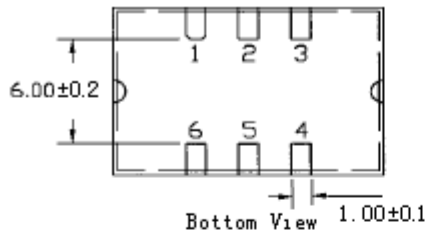
6.1. 10Hz	-80dBc/Hz
6.2. 100Hz	-100dBc/Hz
6.3. 1 KHz	-126dBc/Hz
6.4. 1 0KHz	-140dBc/Hz
6.5. 1 00KHz	-145dBc/Hz

#### 7. Temperature

7.1. Operable temperature range	-40°C to +85°C
7.2. Storage temperature range	-55°C to +125°C



8. Mechanical



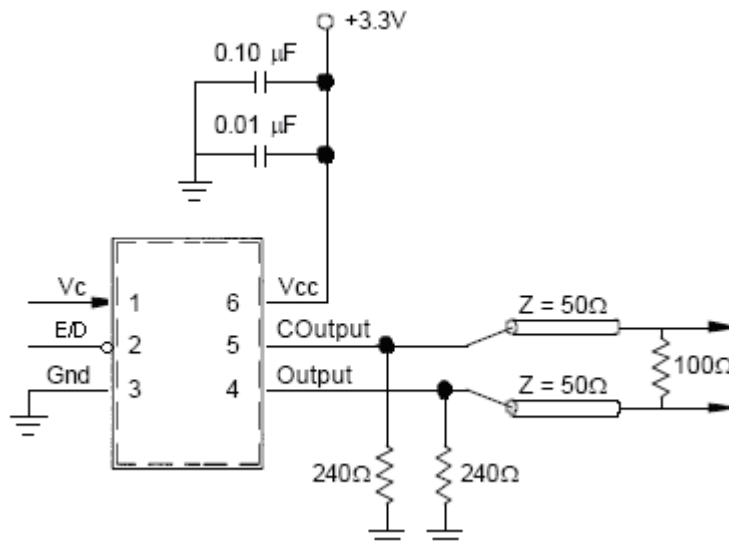
PIN FUNCTION

1	VOLTAGE CONTROL
2	E/D
3	GND
4	OUTPUT
5	OUTPUT
6	Vcc

Note: Enable: "0" or open  
Disable: "1"

Unit : mm

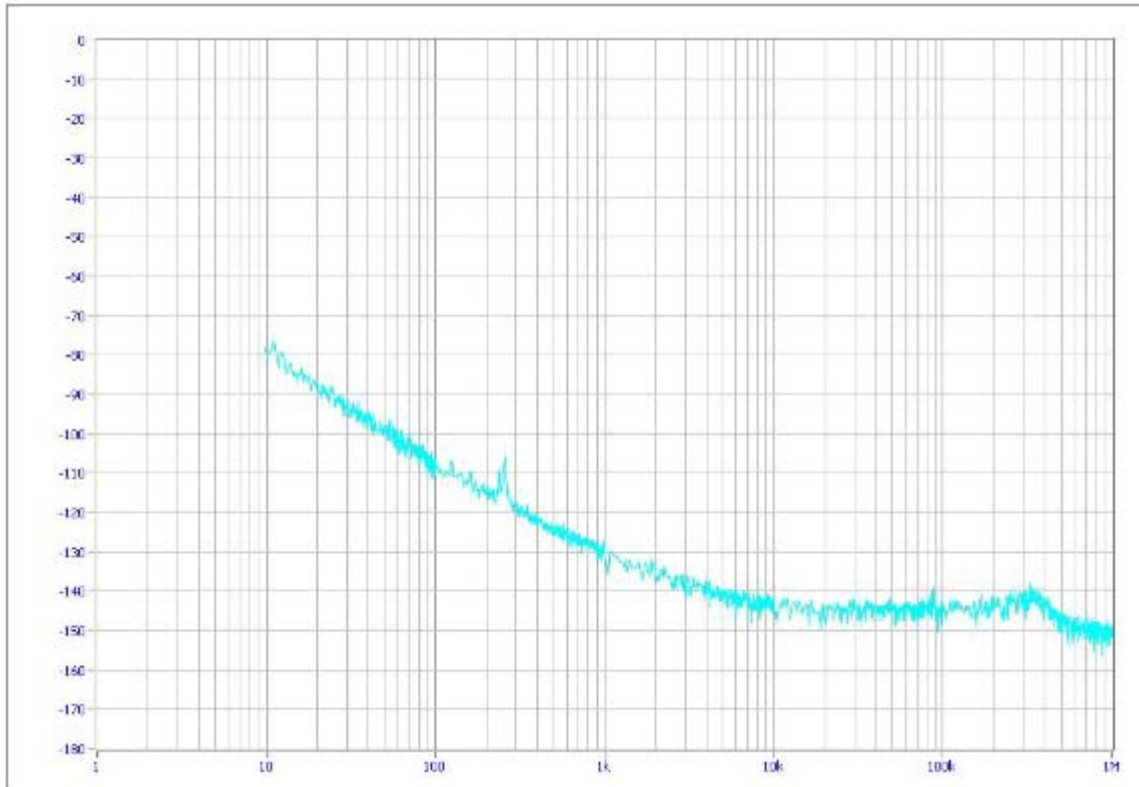
9. Suggested output load configuration



LV-PECL to LV-PECL: For short transmission lengths, the power consumption could be reduced by removing the 100Ω resistor and doubling the value of the pull down resistors.



10. Phase noise



11. Recommended Reflow Profile

