

Travelling Merchant: \_\_\_\_\_

# DATASHEET

Standard: DPBB15630001

Plot			The Label
Drew	Audited	Approved	Stamp, please! Thanks!
Date:			

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## 1、Electrical Parameters

MODEL: DPBB15630001						
No.	Parameters	Condition.	Electrical Spec.			
			Min.	Typ.	Max.	Units
1	Nominal Frequency	FL	156.2543			MHz
2	Oscillation Mode		3rd Overtone			
3	Frequency Stability	Note 1	-20		+20	ppm
4	Operating Temperature		0	~	70	°C
5	Storage Temperature		-55	~	125	°C
6	Supply Voltage	VDD	2.97	3.3	3.63	V
7	Current Consumption	RL=50Ω to VDD-2V	-	-	80	mA
8	Standby Function	Internal Pull Up	Yes			
9	Current Consumption(Standby)	OE=Low	-	-	10	uA
10	Output Type		LVPECL			
11	Output Load		50			Ω
12	Output Voltage High	VoH	VDD-1.025	-	VDD-0.88	V
13	Output Voltage Low	Vol	VDD-1.81	-	VDD-1.62	V
14	Rise Time	20% ~ 80% Output Swing	-	-	1	nS
15	Fall Time	80% ~ 20% Output Swing	-	-	1	nS
16	Symmetry		45	50	55	%
17	Start-up Time	To 90% of Final Amplitude	-	-	10	mS
18	Enable Voltage High (Logic 1)	Note 2	0.7VDD	-	-	V
19	Enable Voltage Low (Logic 0)	Note 2	-	-	0.3VDD	V
20	Output Disable Delay Time	T off	-	-	500	μS
21	Output Enable Delay Time	T on	-	-	500	μS
22	Phase Jitter	12K ~ 20MHz	-	-	0.2	pS rms
23	Vibration	Test Condition: 0.75mm ;acceleration:10g;10Hz~2000Hz, one cycle per 30 min, test 2 hour. (3 times for each 3 directions X , Y , Z). IEC 68-2-06 Test Fc.				
24	Shock	100g; 6ms; half sine wave (3 times for each 3 directions X , Y , Z),IEC 68-2-27 Test Ea/Severity 50A.				

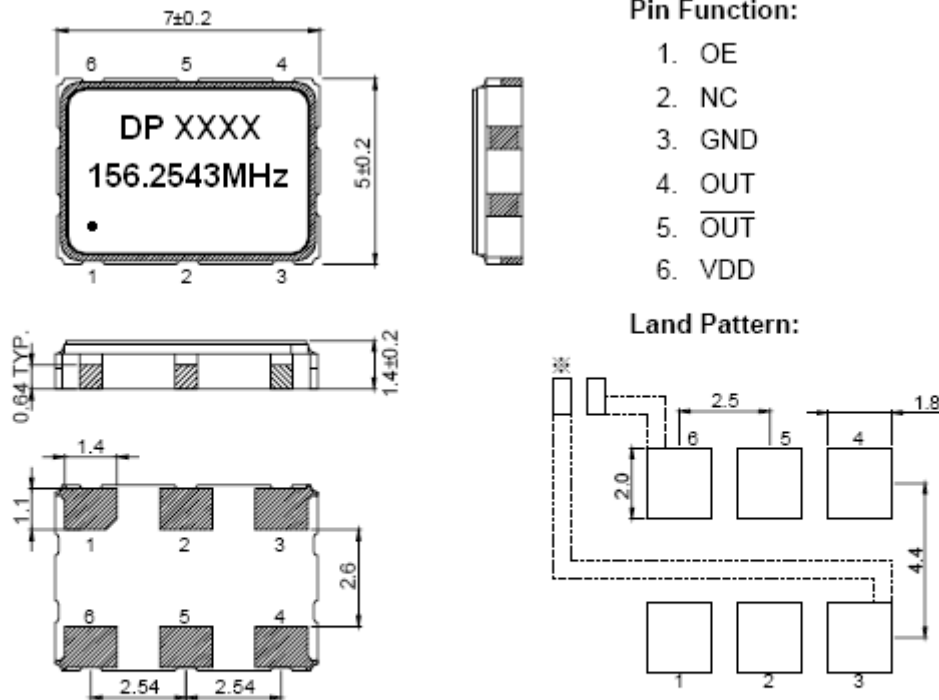
**Note 1:** Inclusive of frequency tolerance at 25degC, variation over temperature, supply voltage variation, aging and vibration.

**Note 2:** Output will be enable if OE is Logic 1 or open ; Output will be disable if OE is Logic 0.

**Note 3:** The standard testing environment except temperature test is 25±5degC, 40%~70% relative humidity.



## 2、Mechanical Structure(mm)



### Pin Function:

- 1. OE
- 2. NC
- 3. GND
- 4. OUT
- 5.  $\overline{\text{OUT}}$
- 6. VDD

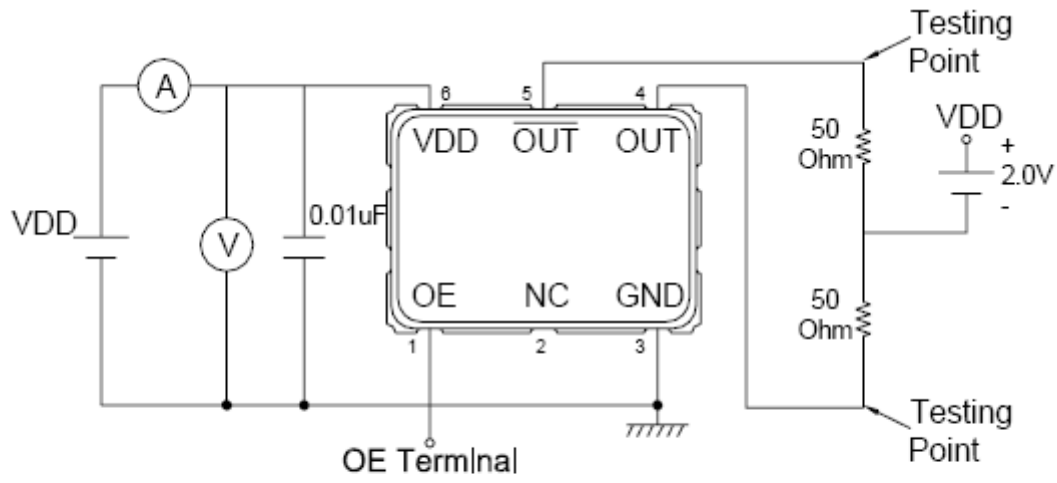
### Land Pattern:

※ Pad dimension tolerance  $\pm 0.2$  mm      ※ Power Supply Decoupling Capacitor is Required.

**Note: Play inversion between kikuchi map instructions:**

- 1)The first two xx representative : year
- 2)After two xx representative : week

## 3、Testing Circuit

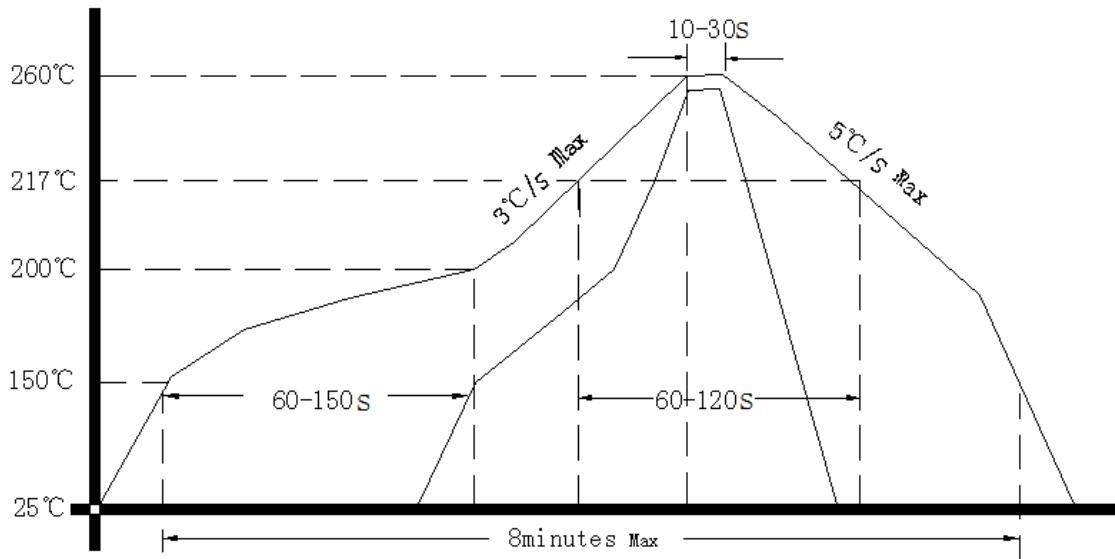


### Testing Circuit Note:

- 1) Above testing circuits cover all the specifications except temperature test & Jitter measurement.
- 2) All the testing equipments are 50Ohm terminal.
- 3) OE terminal is open connection except OE function test.



#### 4、 Suggested Reflow Profile (RoHS)



#### 5、 Package: Tape & Reel (mm)

