

Travelling Merchant: \_\_\_\_\_

# DATASHEET

Standard: OSC5310W-77.76MHz

Plot			The Label
Drew	Audited	Approved	Stamp, please! Thanks!
Date: 2009-09-16			

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## The Parameter of Product

### MODEL: OSC5310W-77.76MHz

#### 1. Output

1.1. Frequency	77.76MHz
1.2. Waveform	HCMOS
	$V_{OH} \geq 90\% V_{DD}$ , $V_{OL} \leq 10\% V_{DD}$
1.3. Duty cycle	40%~60% @50%
1.4. Rise/fall time(10%~90% )	$\leq 8ns$
1.5. Warm-up time	10ms
1.6. Load	15pF $\pm$ 10%

#### 2. Frequency Stability

2.1. Temperature Stability	$\leq \pm 5.0 \times 10^{-5}$ (All condition* ) @-40°C ~ +85°C ref. to @25 °C
2.2. Initial Frequency	$\leq \pm 2.0 \times 10^{-5}$ @25°C
2.3. Aging	$\leq \pm 5.0 \times 10^{-6}$ / first year

#### 3. Power Supply

3.1. Operating Voltage	+3.3VDC (unalterable)
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#### 4. Current

4.1. Operating Current	30mA (Typical)
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#### 5. Environment Characteristics

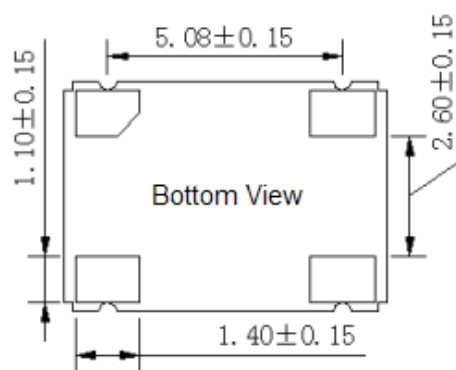
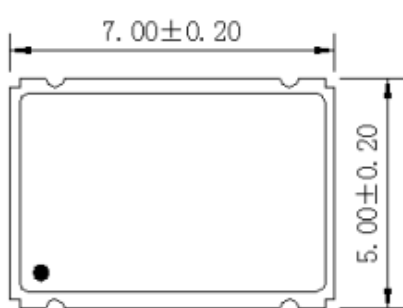
5.1. Operating Temperature	-40°C to +85°C
5.2. Storage Temperature	-55°C to +125°C
5.3. Relative Humidity	25% to 80%

**All condition\*:** Include 25°C tolerance, operating temperature range , input voltage change, aging, load change, shock and vibration.

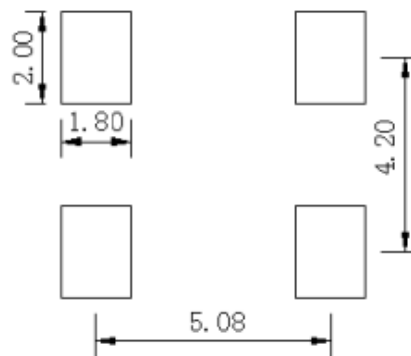
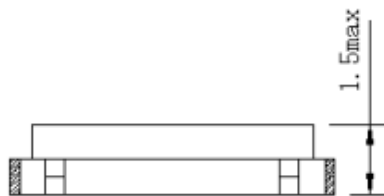


6. Mechanical Structure

- Outline Dimensions (mm)



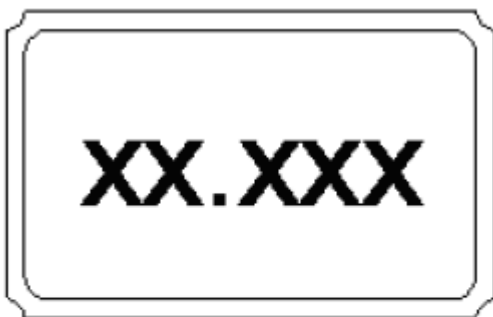
Recommended solder pattern



Pin	Connection
1	INH
2	GND
3	Output
4	V <sub>DD</sub>

Note: 0.01μF bypass capacitor should be placed between V<sub>DD</sub> (pin4) and GND (pin2) to minimize power supply line onise.

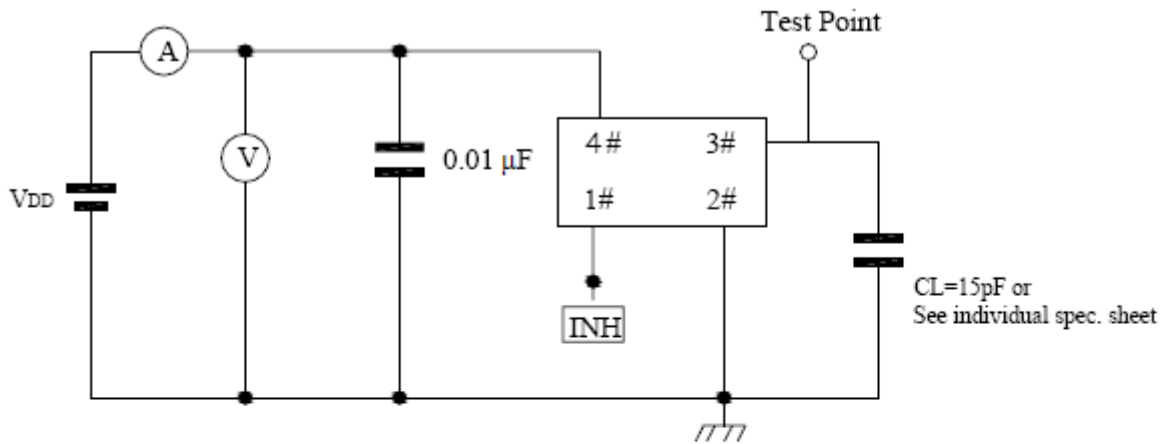
- MARKING



6 Digitals: Frequency in MHz

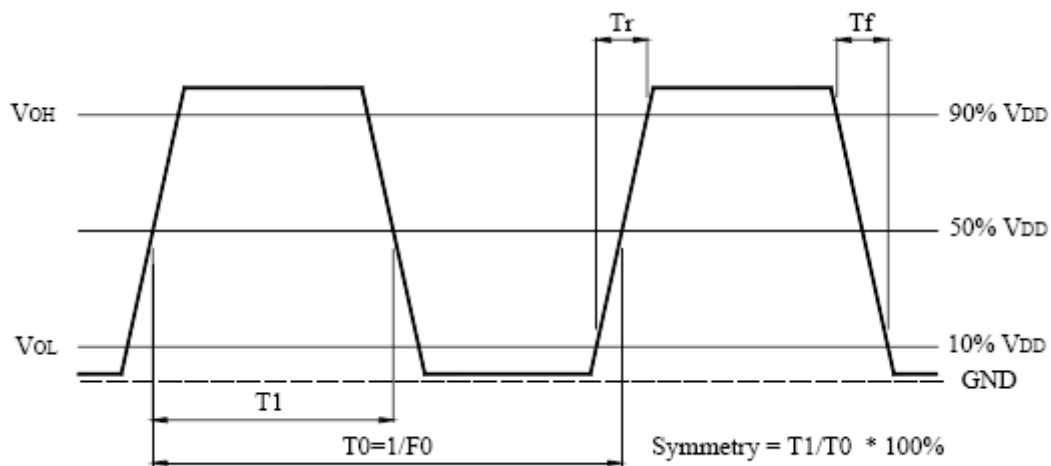


7. HCMOS test circuit



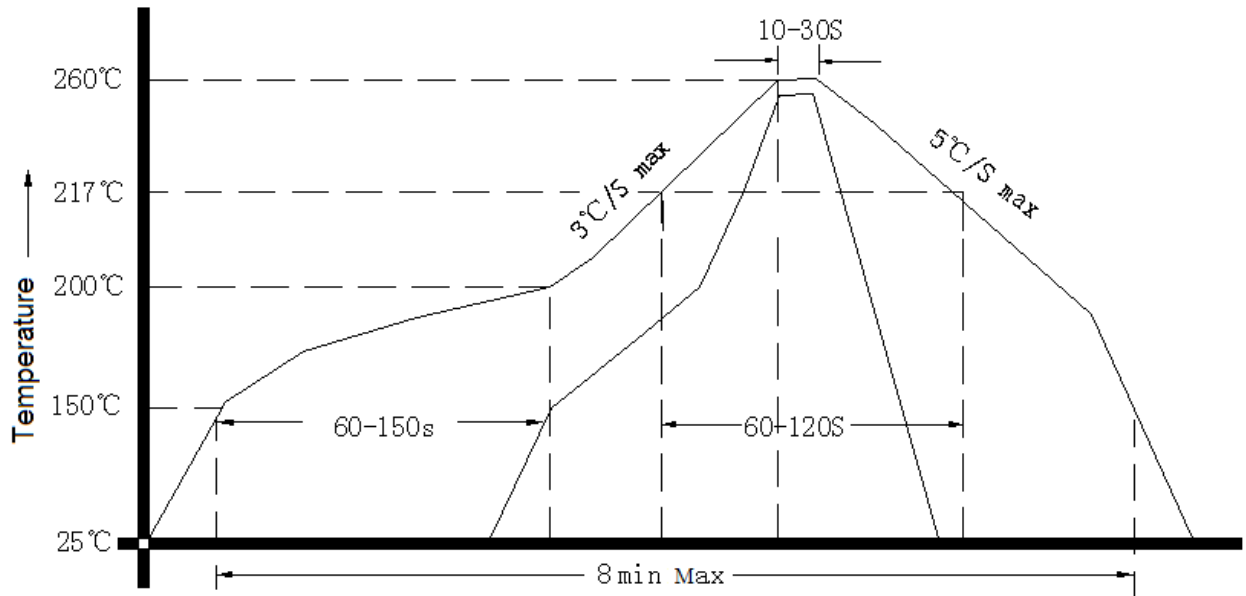
Enable/Disable Function	
Input (pin 1)	Output (pin 3)
Open	Enable
$V_{IH} \geq 0.8V_{DD}$	Enable
$V_{IH} \leq 0.3V_{DD}$	Disable

8. HCMOS output waveform



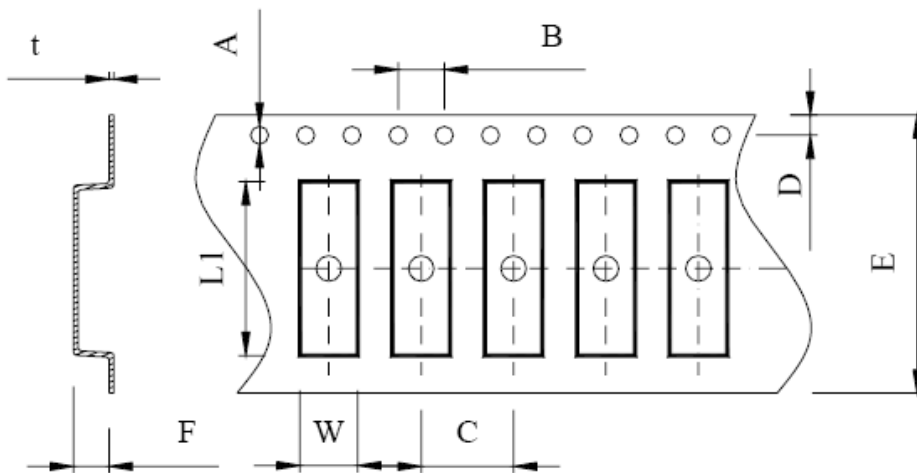


9. Reflow Soldering

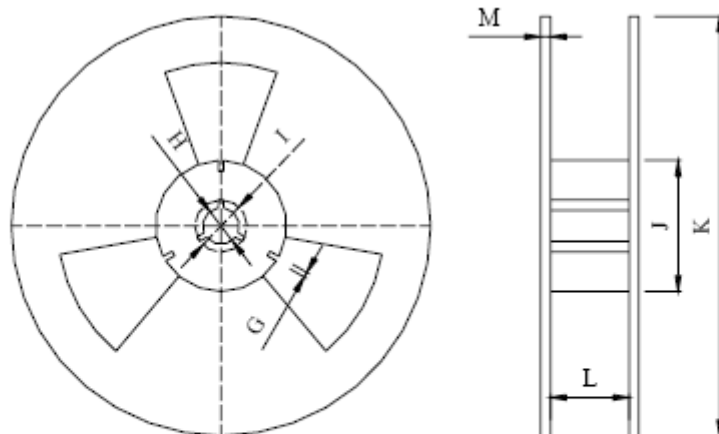




10. Reel Dimensions(mm)



A	B	C	D	E	F	L1	W	t
1.50	4.0	4.0	1.75	8.0	1.0	7.5	5.4	0.3



G	H	I	J	K	L	M
2.5	13.5	21.6	60.0	178	17.5	1.6

\*1000pcs/Reel