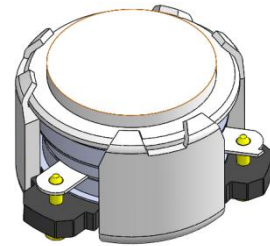




DP0159C

3600MHz to 3800MHz Single-Junction Surface Mount Circulator

Revision A

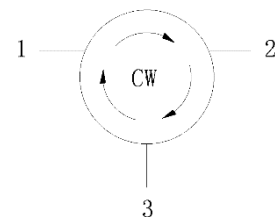


Applications:

- Wireless Infrastructure
- Power Amplifier

Features:

- Operating frequency range: 3600MHz to 3800MHz
- Operating temperature range: -40°C to +105°C
- Storage temperature range: -65°C to +155°C
- Small surface-mount package delivered on T&R
- BeO free & RoHS compliant



Block Diagram

**Electrical Specifications:**

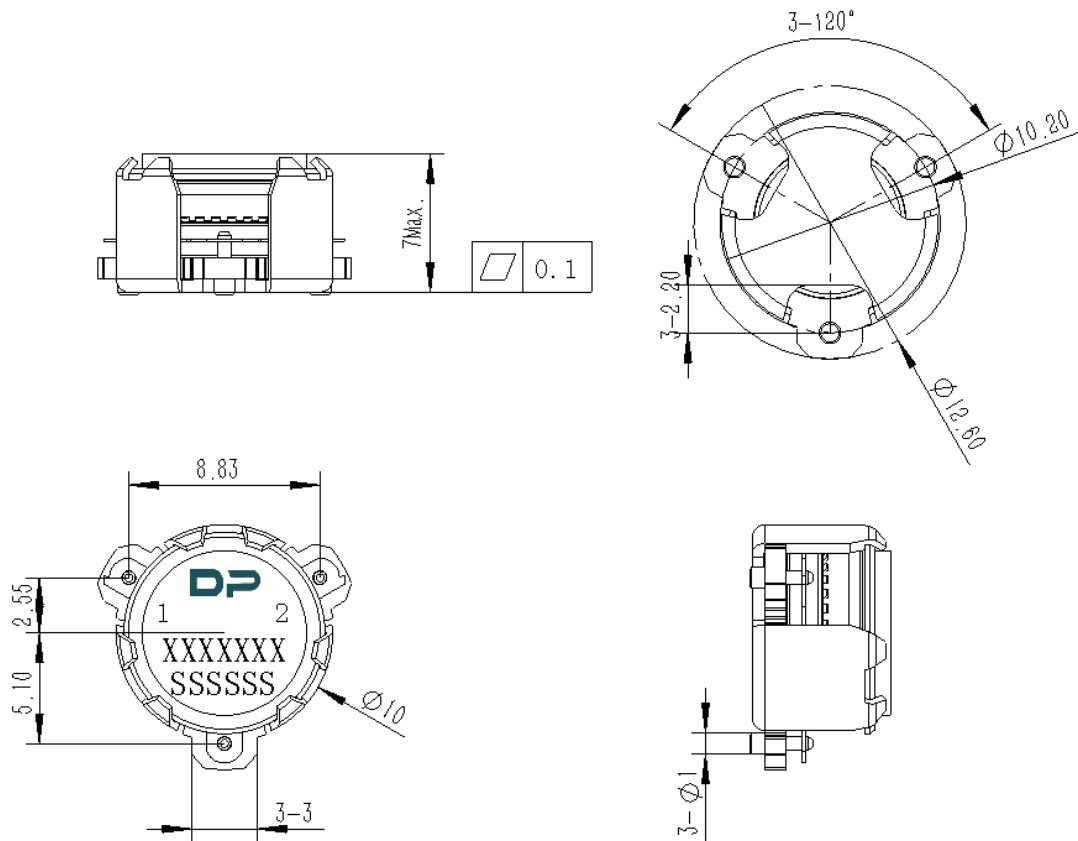
ITEM	SPECIFICATION	
Frequency	3600~3800	MHz
Direction	CW	
Impedance	Typ: 50	Ω
Insertion Loss (Max.)	0.19@25°C 0.23@-40~+105°C	dB
Isolation (Min.)	22@3600~3800MHz 16@3400~4000MHz	dB
Return Loss (Min.)	22	dB
3rd IMD (Max.)	60@2x5W CW tones, 5MHz spacing	dBc
Group delay	2	ns
2nd harmonic	10	dBc
3rd harmonic	5	dBc
Power FWD/REV/PEAK	20/20/160	W
Termination/Attenuator	/	W/dB
Out Band Resonance	Resonance point away 3400MHz-4000MHz	
Input Impedance ,real	46 Min@3600MHz 52 Typ@3600MHz 55 Max@3600MHz 46 Min@3700MHz 49.5 Typ@3700MHz 54 Max@3700MHz 46 Min@3800MHz 48 Typ@3800MHz 54 Max@3800MHz	Ω
Input Impedance ,imaginary	-6j Min@3600MHz 3j Max@3600MHz -4.5j Min@3700MHz 3j Max@3700MHz -2j Min@3800MHz 5.5j Max@3800MHz	j Ω

Notes :

1. Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.
2. Performance is guaranteed under the conditions listed in this table and over the operating temperature range.
3. Performance will not degrade by > 10% (Insertion loss > 20%) with an operating temperature of up to 130 °C.



Mechanical Specifications:



Unit : Millimeters

Notes:

1. The housing and pins are silver-plated.
2. Tolerance ± 0.2 mm unless otherwise specified.
3. Co-planarity Specification: 0.1mm maximum.
4. Part Number, Lot Code, and Port Designation are printed on the top side of device
5. Serial Number format shall be SSSSSS



Packaging Style:

ITEM	W	A0	B0	C0	K0	D	E	F	F2	P	P0	P2	T
DIM	32	10.8	12	4	7.4	Φ1.5	1.75	14.2	28.5	24	4	2	0.5
TOLE	±0.3	±0.2	±0.2	±0.2	±0.2	+0.1	±0.1	±0.15	±0.1	±0.1	±0.1	±0.1	±0.05

